FAKEBP

CMS CSC DAQMB Fake BackPlane

This board will plug in DAQMB   --- Feb. 2001

ELECTRONICS LAB
PHYSICS DEPARTMENT
THE OHIO STATE UNIVERSITY
174 WEST 18TH AVE
COLUMBUS OHIO 43210
These connectors are MALE RIGHT ANGLE, and plug in DAQMB connectors directly

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TMB[14:0]: CLCT/TMB data to 18-bit fifo
TMB[29:15]: Additional CLCT/TMB data to 36-bit fifo
TMB30: DDU special word flag
TMB31: Last word flag bit
TMB32: 40MHz clock to write to FIFO
TMB33: Fifo write_enable inverted
Spare[7:1]: Used to exchange information in CFEB calibration mode
CCB data are GTLP, TMB data are LVTTL.