The 'B' signals are for in between pairs.

ENOUT VOLTAGE DIVIDER CIRCUIT FOR LVDS INPUT.
COMMON TO ALL SCA'S

Stuff EITHER R26 or R27, NOT BOTH!

FILTER CAPS FOR AD8011 AND V5PAADC - NEXT PAGE

Group G1 (and GA1)
The 'B' signals are for in between pairs.

Generally, one filter CAP per power pin, one TANT per chip.

"V5PDADC"

"V5PAADC"

Watch placement of ADCCLK termination!
Comparator Data MUX

Comparator Connectors to neighbor CFEBS

CFEB to LCT99 Connector

<<==LVDS Receiver for Comparator signals from LCT99

<<==Clock Driver for 40MHz Comparator clock

Three filter CAPs per power pin on Channel Links (.1, .01, .001uF).
Generally, one CAP per power pin and one tantalum per chip.

For MUX chips

For LVDS drivers

Common

THE OHIO STATE UNIVERSITY
PHYSICS DEPARTMENT ELECTRONICS LAB
174 WEST 18TH AVE. COLUMBUS OH 43210

LVDS Receiver system for digital SCA inputs
Cathode Front End Card
CMS CSC Electronics

JRG

1-31-2005 16:00

CFEBVTX
The 'B' signals are for in between pairs.
Put the op-amps near the branch point of the traces!
The 'B' signals are for in between pairs.

Staff EITHER R26 or R27, NOT BOTH!
Consider SMT/Resettable fuses in the future...

The 'B' signals are for in between pairs.

Watch placement of ADCCLK termination!

Generally, one filter CAP per power pin, one TANT per chip.
Put the op-amps near the branch point of the traces!
Calibration Cap added to each channel.

One filter CAP per power pin, one TANT per chip.

Free Jumper nos:
Norm: 47-
Spec G: 8-

Free CAP nos:
Spec G: 8-
Spec G: 19-

Gain1X
Gain2X
Gain7X
Gain8X
Gain9X
Gain10X
Gain11X
Gain12X
Gain13X
Gain14X

PDA3G4
PDA9G4
PDA4G4
PDA8G4
PDAAG4
PDACG4
PDAGG4

U8G4
U8G4

0.1uF
0.1uF

R1G4
R3G4
R6G4
R7G4
R10G4
R12G4

999G
100K
2.4K

0.01uF
0.01uF
0.01uF
0.01uF
0.01uF
0.01uF
0.01uF
The 'B' signals are for in between pairs.
Consider SMT/Resettable fuses in the future...

The 'B' signals are for in between pairs.
Put the op-amps near the branch point of the traces!
The 'B' signals are for in between pairs.

Stuff EITHER R26 or R27, NOT BOTH!
The 'B' signals are for in between pairs.

Watch placement of ADCCLK termination!

Generally, one filter CAP per power pin, one TANT per chip.

SCA- ONE 0.1UF AND 0.001UF PER POWER PIN

Group G5 (and GA3)
Put the op-amps near the branch point of the traces!
The 'B' signals are for in between pairs.

Staff EITHER R26 or R27, NOT BOTH!
Generally, one filter CAP per power pin, one TANT per chip.
Put the op-amps near the branch point of the traces!