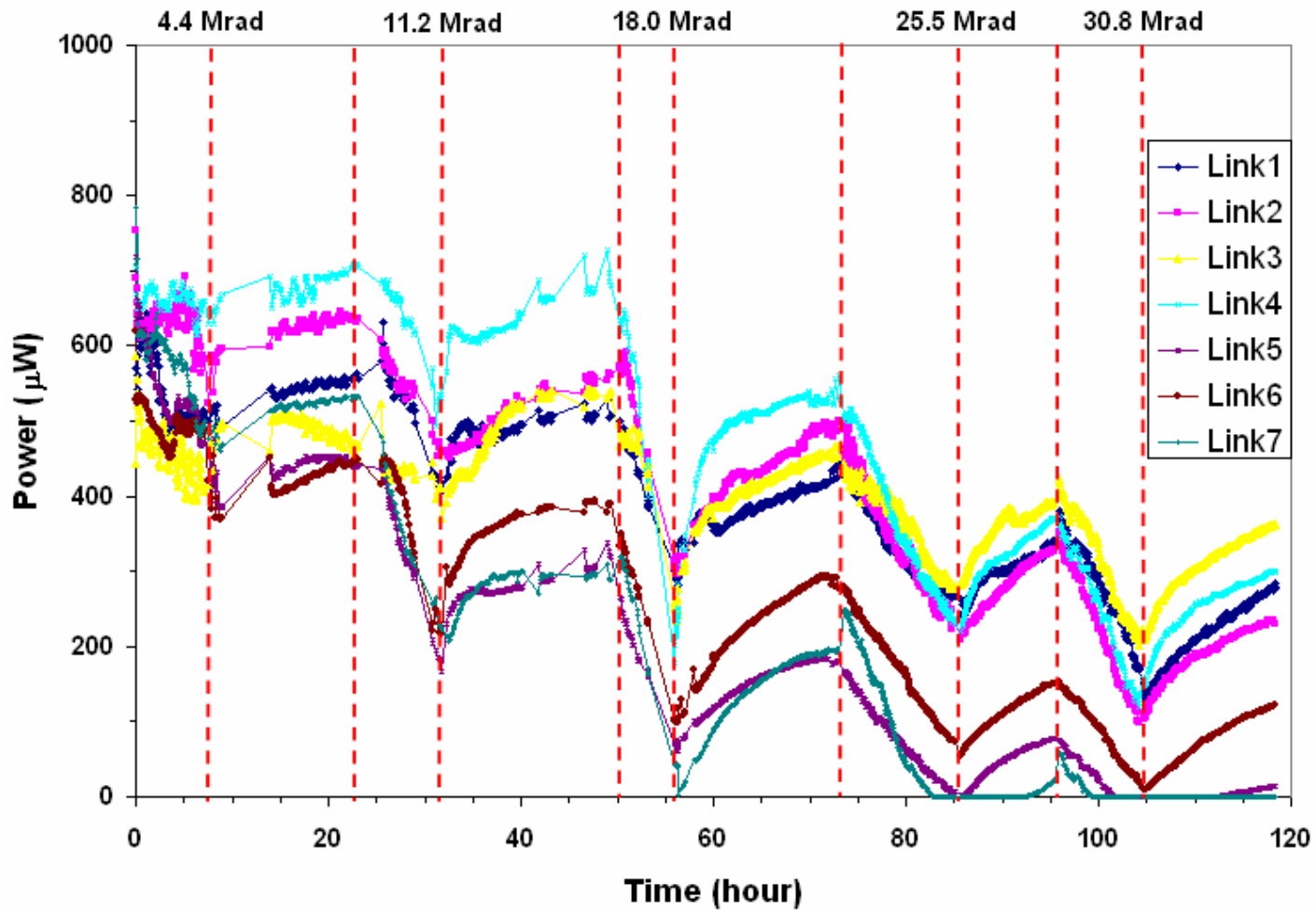


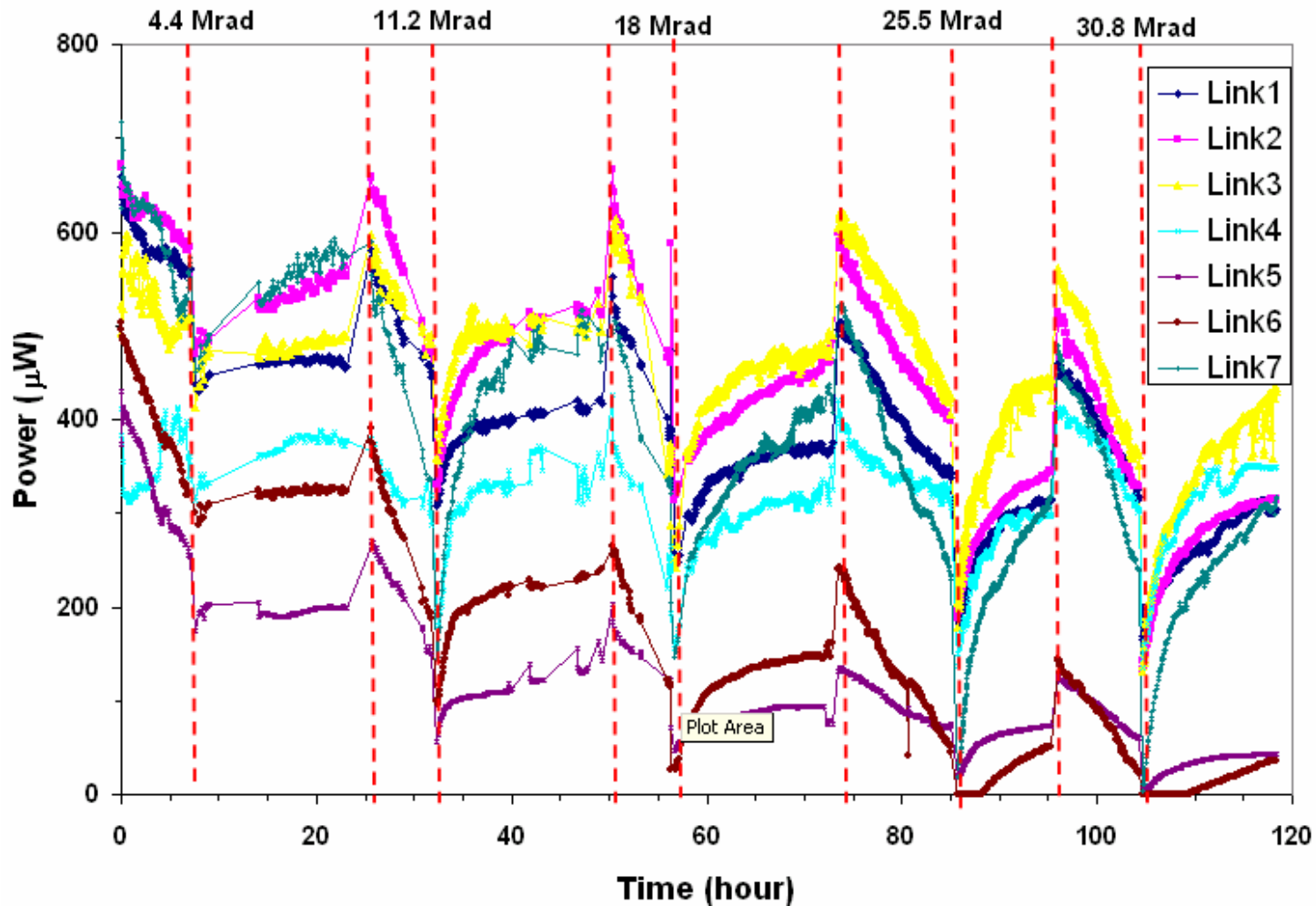
Opto-board #1 (BeO#3) Clock Optical Power



Anneal at Iset of 1.5 mA

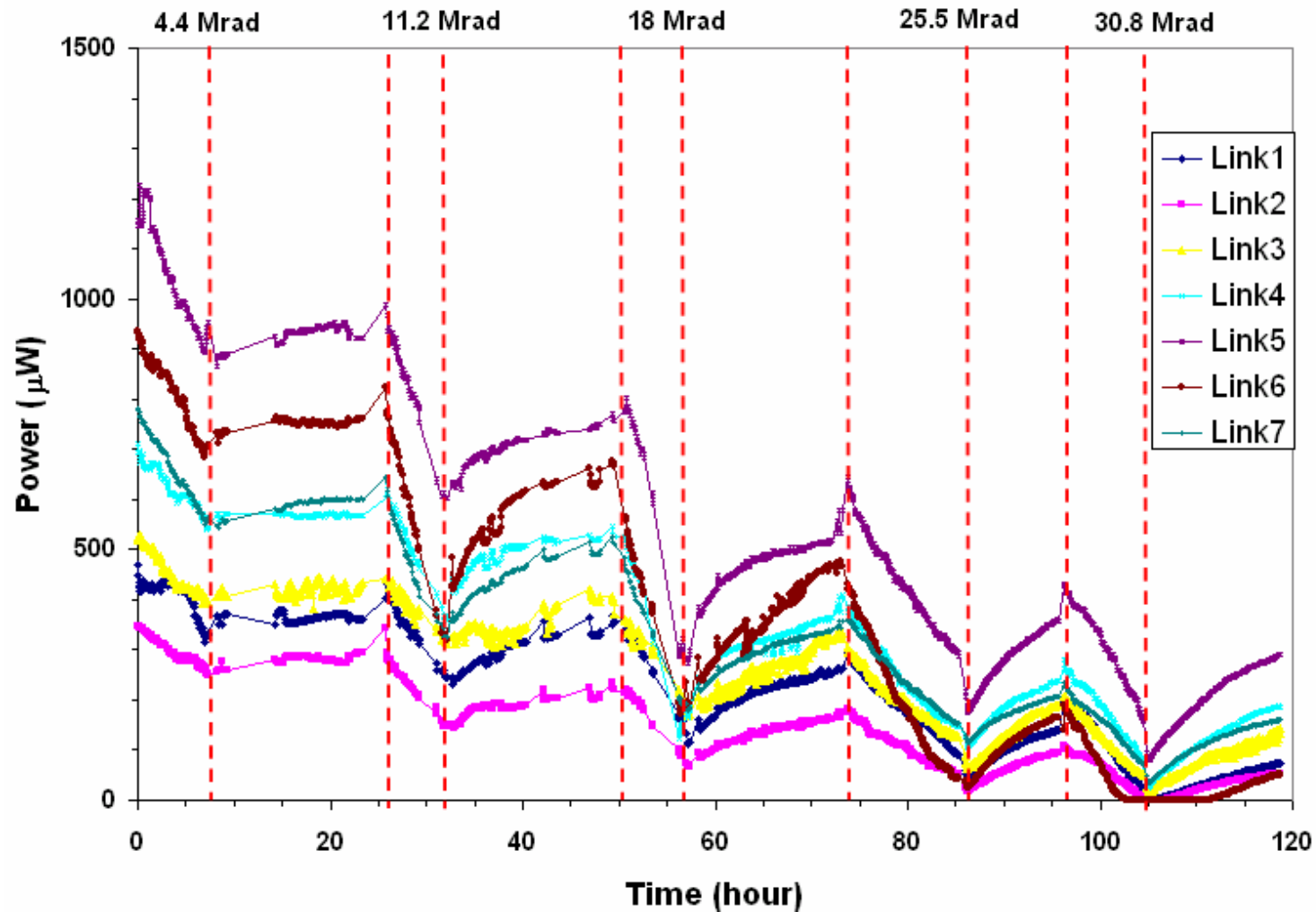
Beam at Iset 0.6 mA

Opto-board #1 Data Optical Power



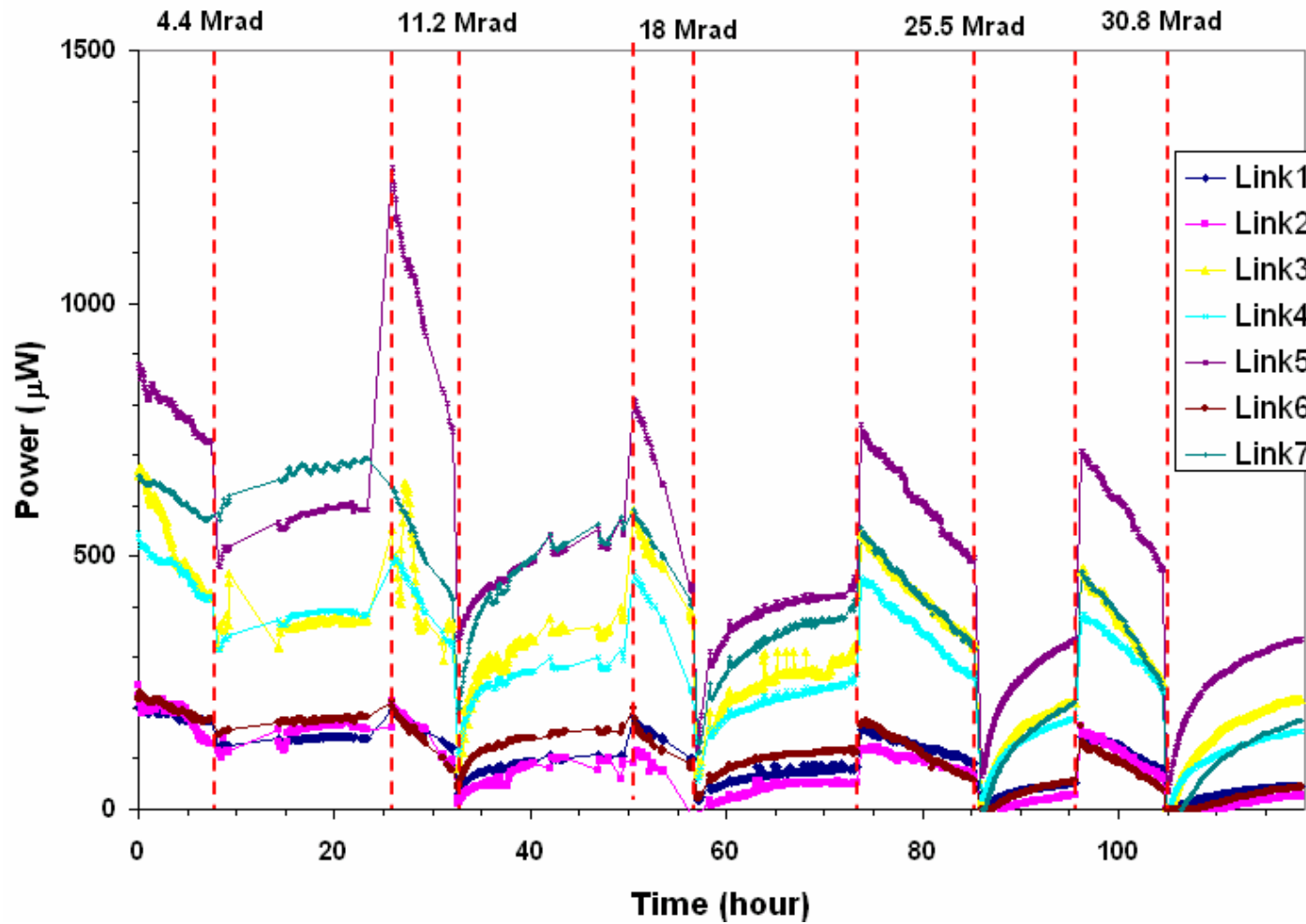
Anneal at Iset of 1.5 mA and all high
Irradiate at least of 0.6 mA and PRBS

Opto-board #2(BeO#4) Clock Optical Power



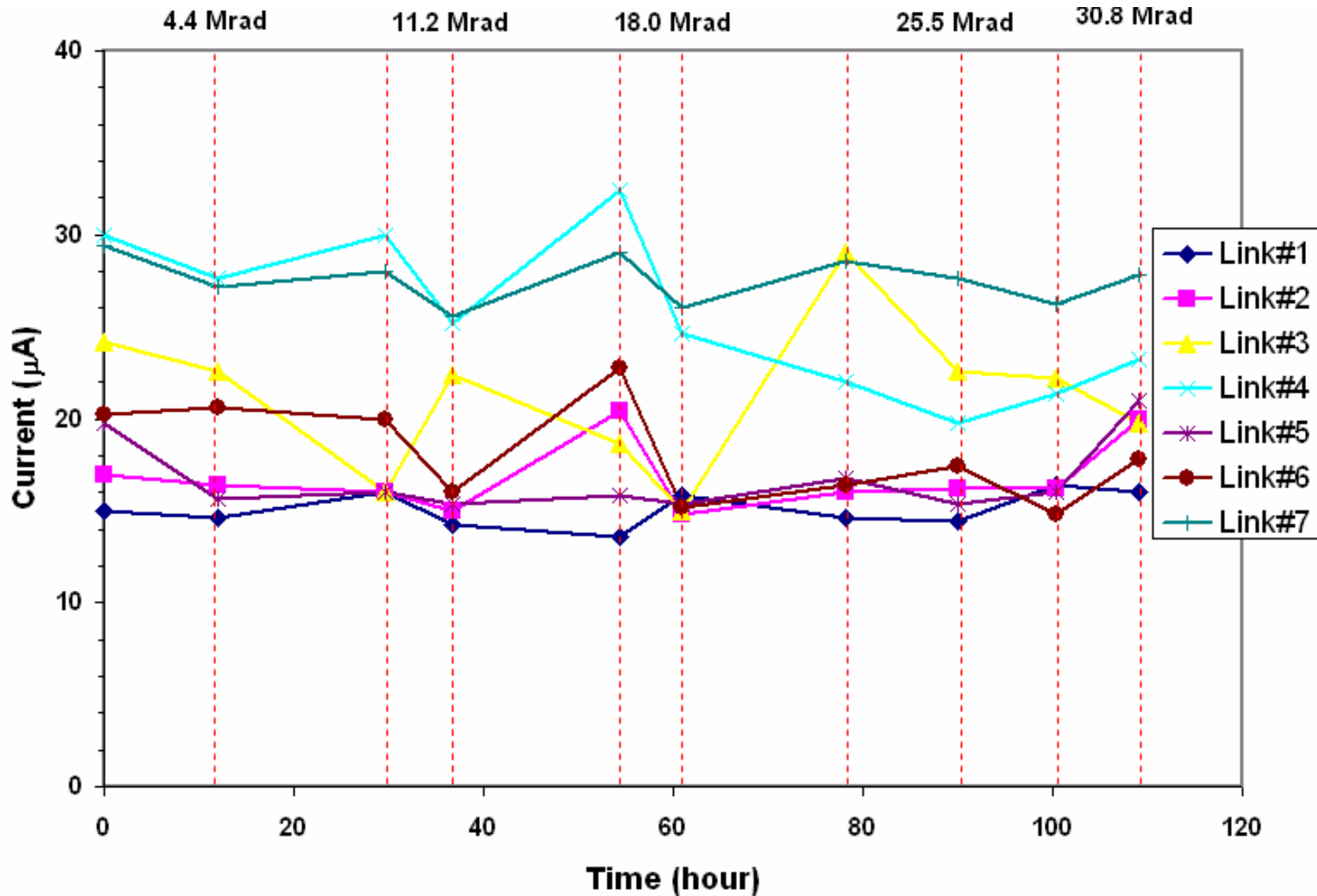
Anneal at Iset of 1.5 mA
Beam at Iset 0.6 mA

Opto-board #2 Data Optical Power



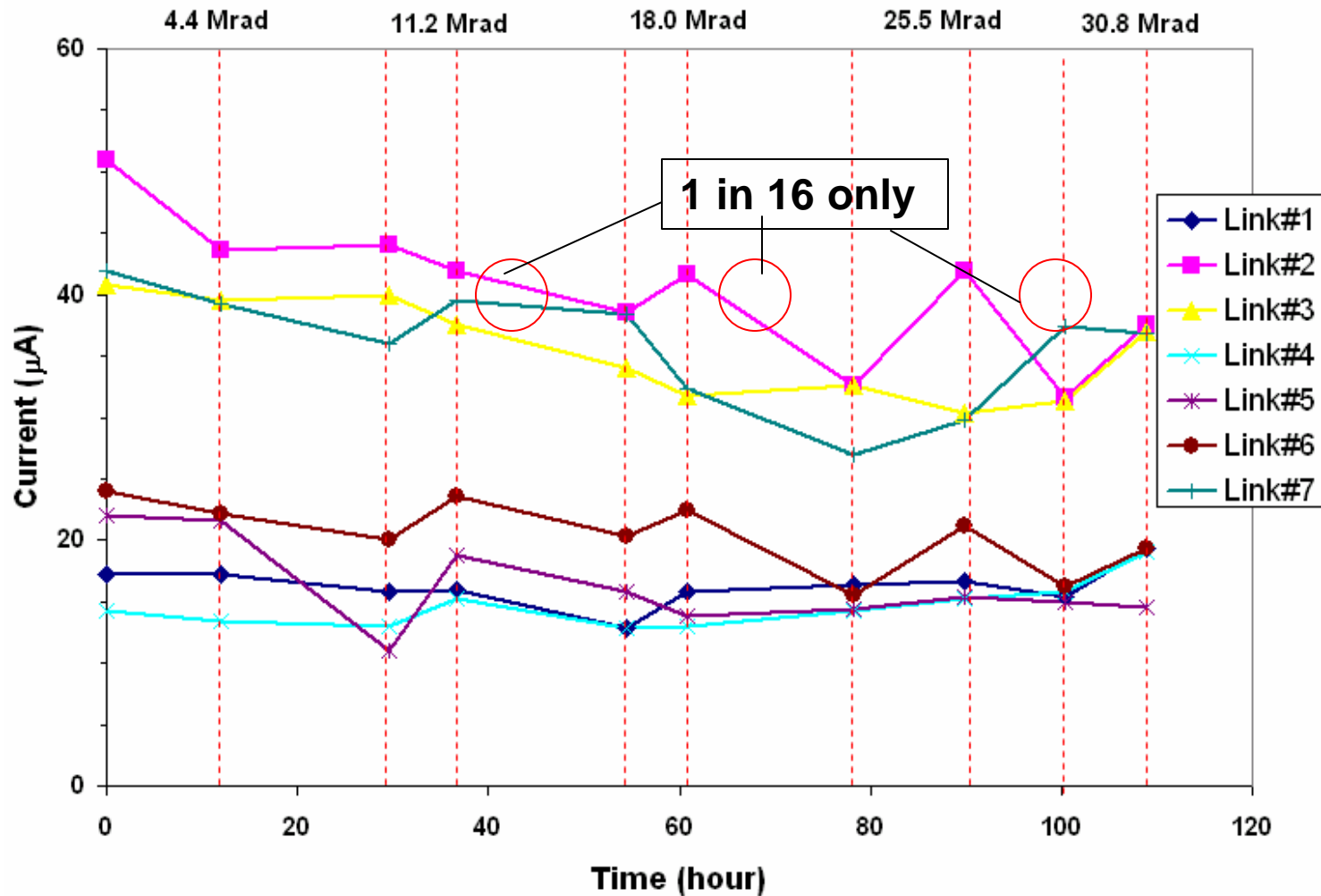
Anneal at Iset of 1.5 mA and all high
Irradiate at lest of 0.6 mA and PRBS

Opto-board #1 Threshold



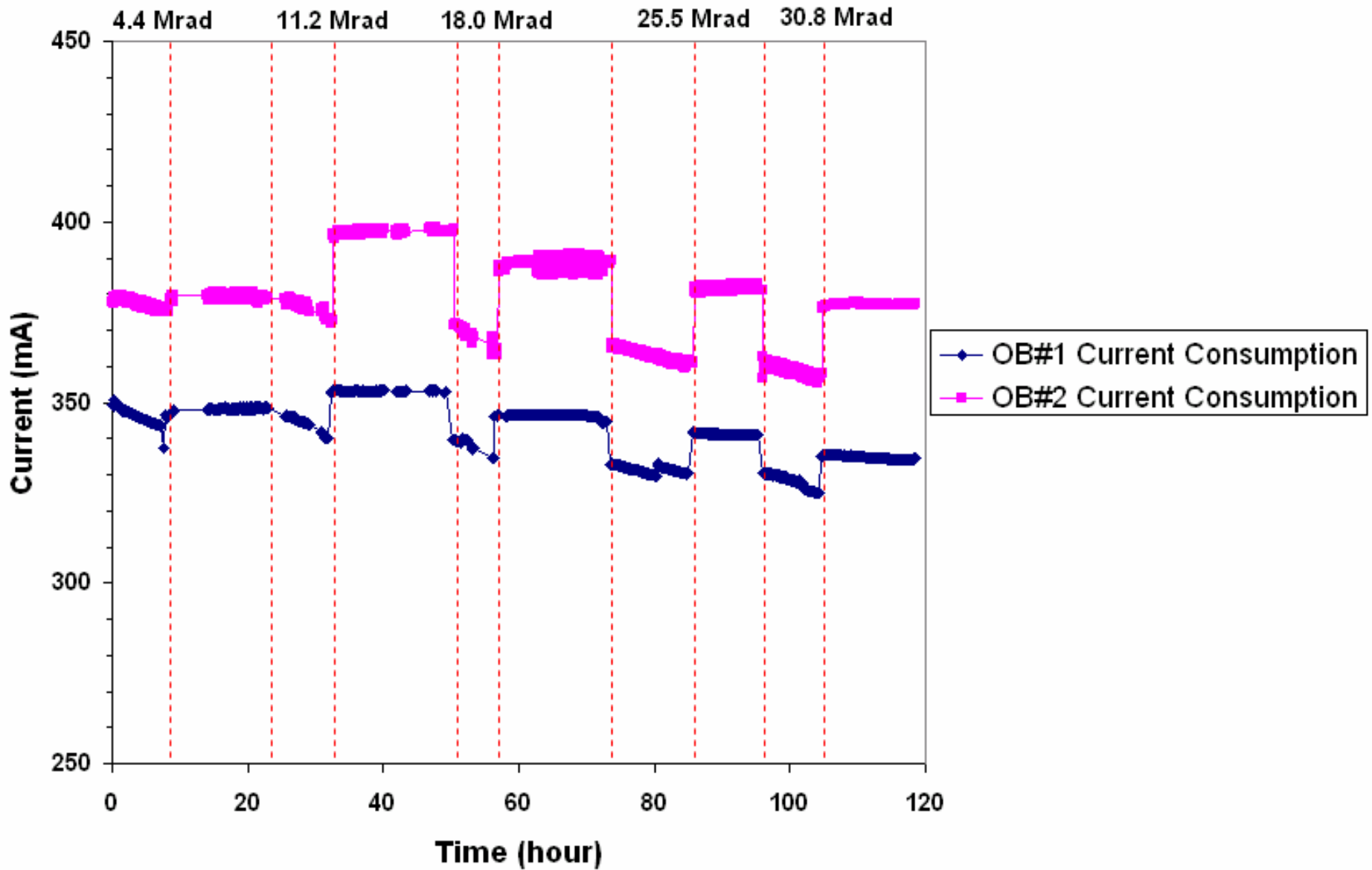
- all other links at PRBS and $I_{pin} = 100 \mu A$

Opto-board #2 Threshold

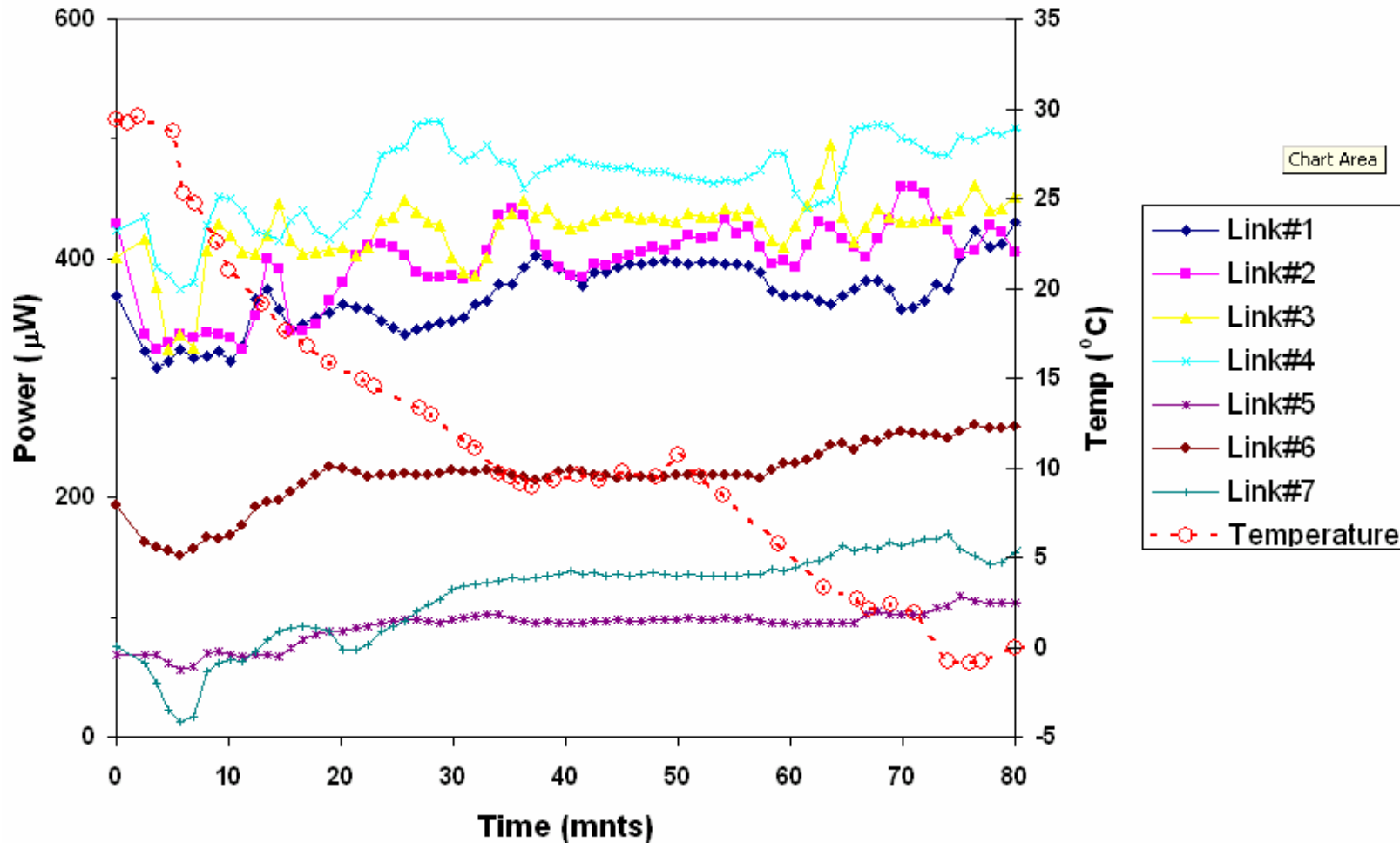


- Because of low optical power in link #2 the measurements 4, 6 and 8 could only be done with 1 in 16 data signal
- all other links at PRBS and $I_{\text{pin}} = 100 \mu\text{A}$

Opto-borads #1 and #2 Current Consumption

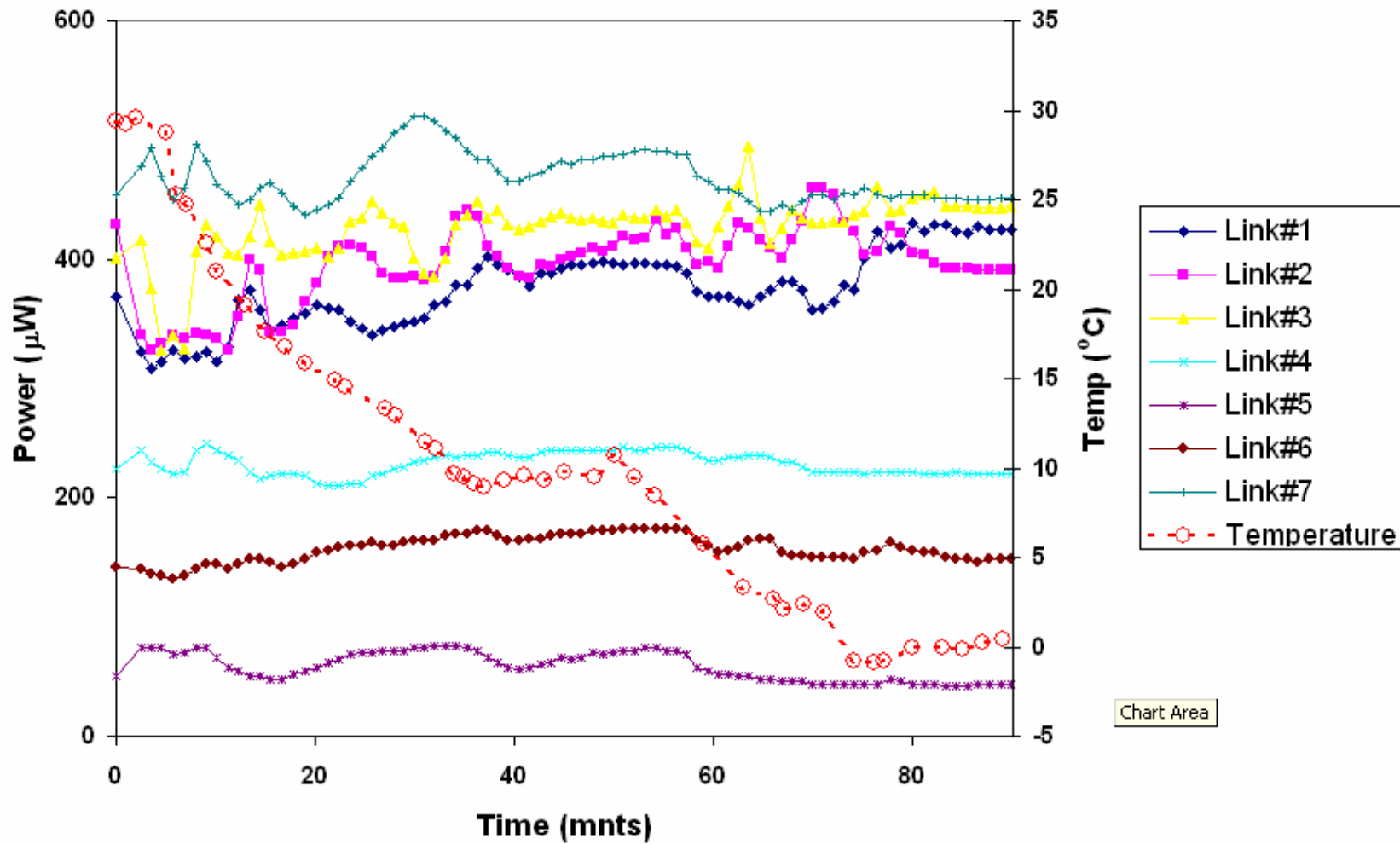


Opto-board #1 Clock Power vs Temp.

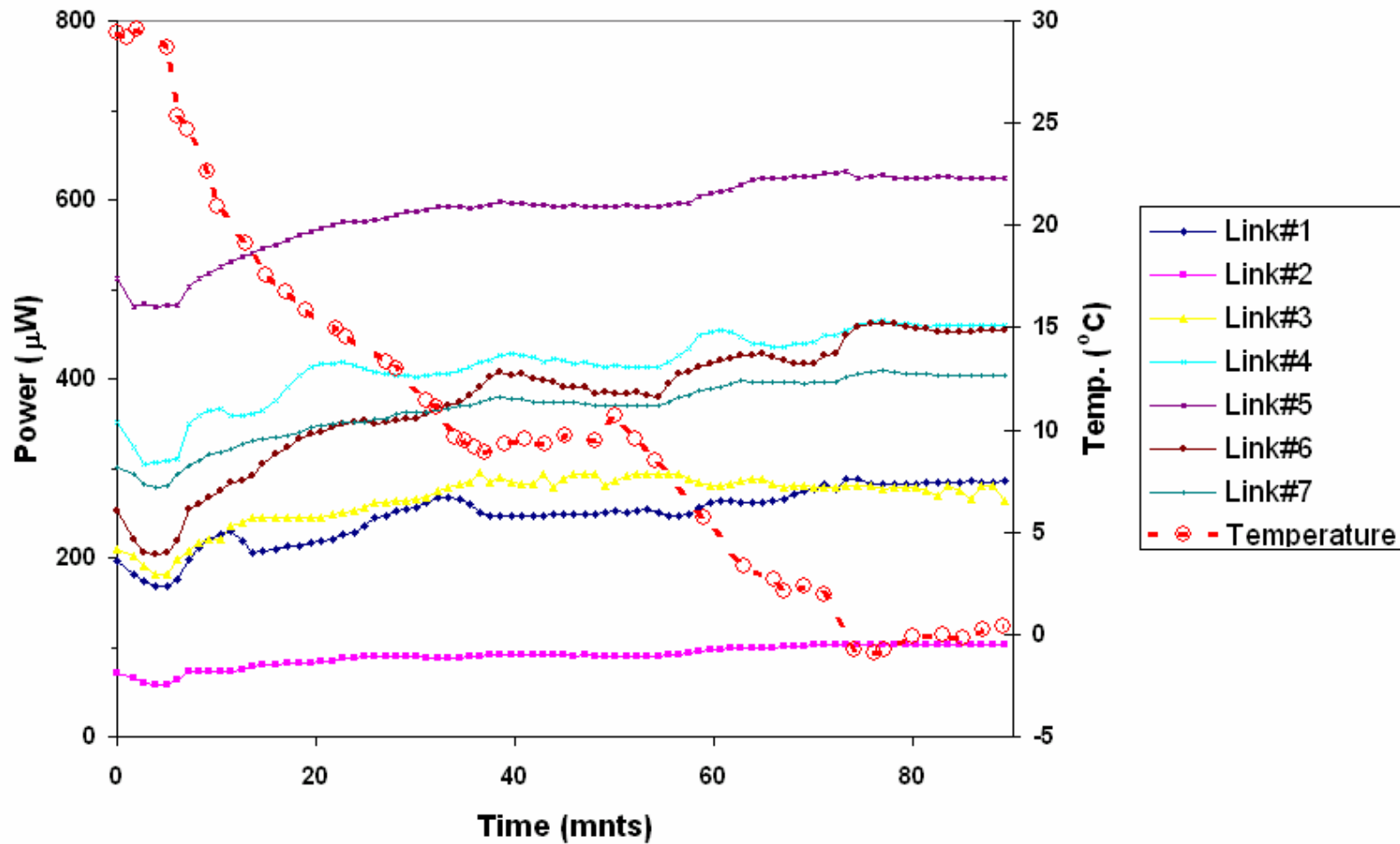


- measured with the long cable (same with the next three plots)

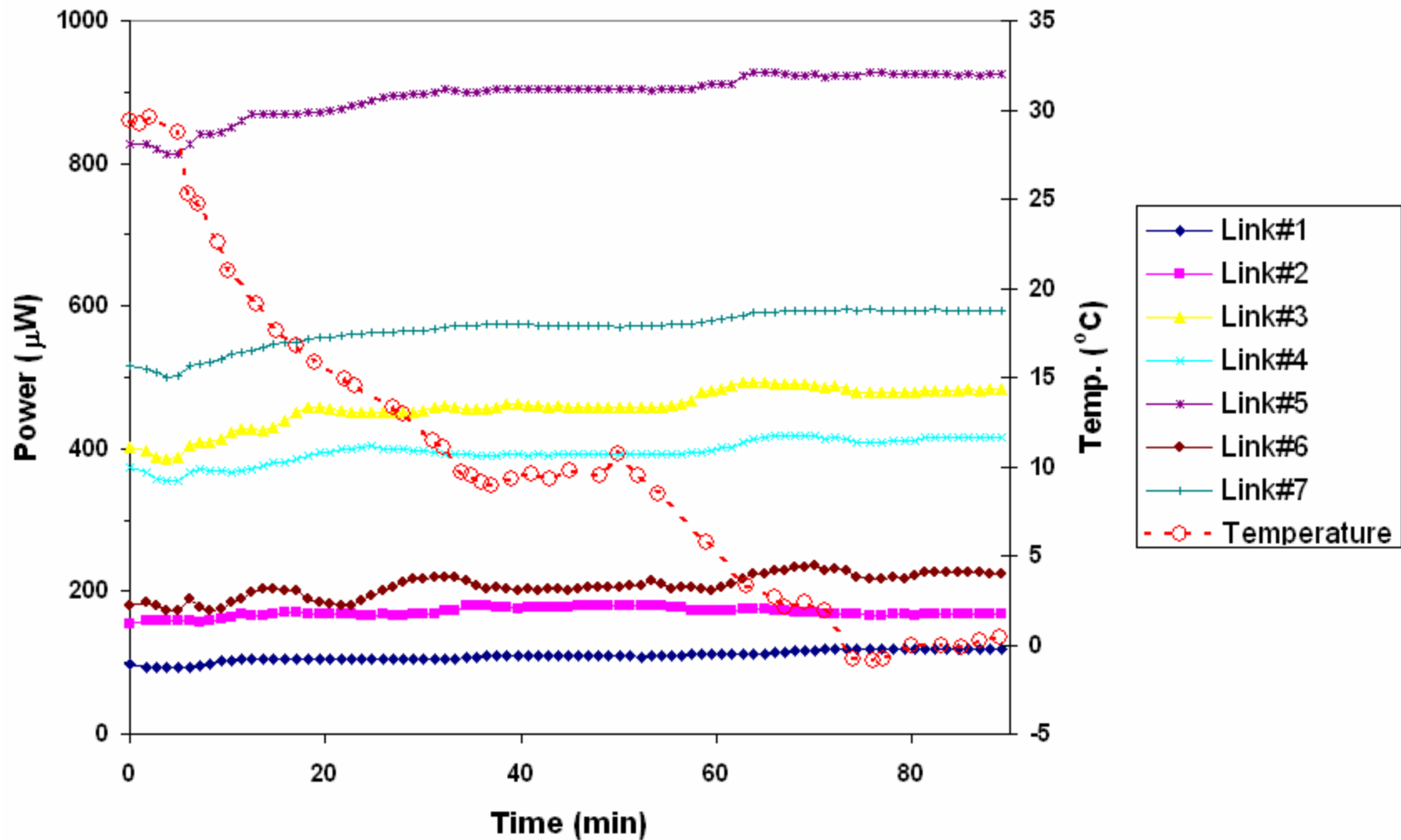
Opto-board #1 Data Power vs Temp.

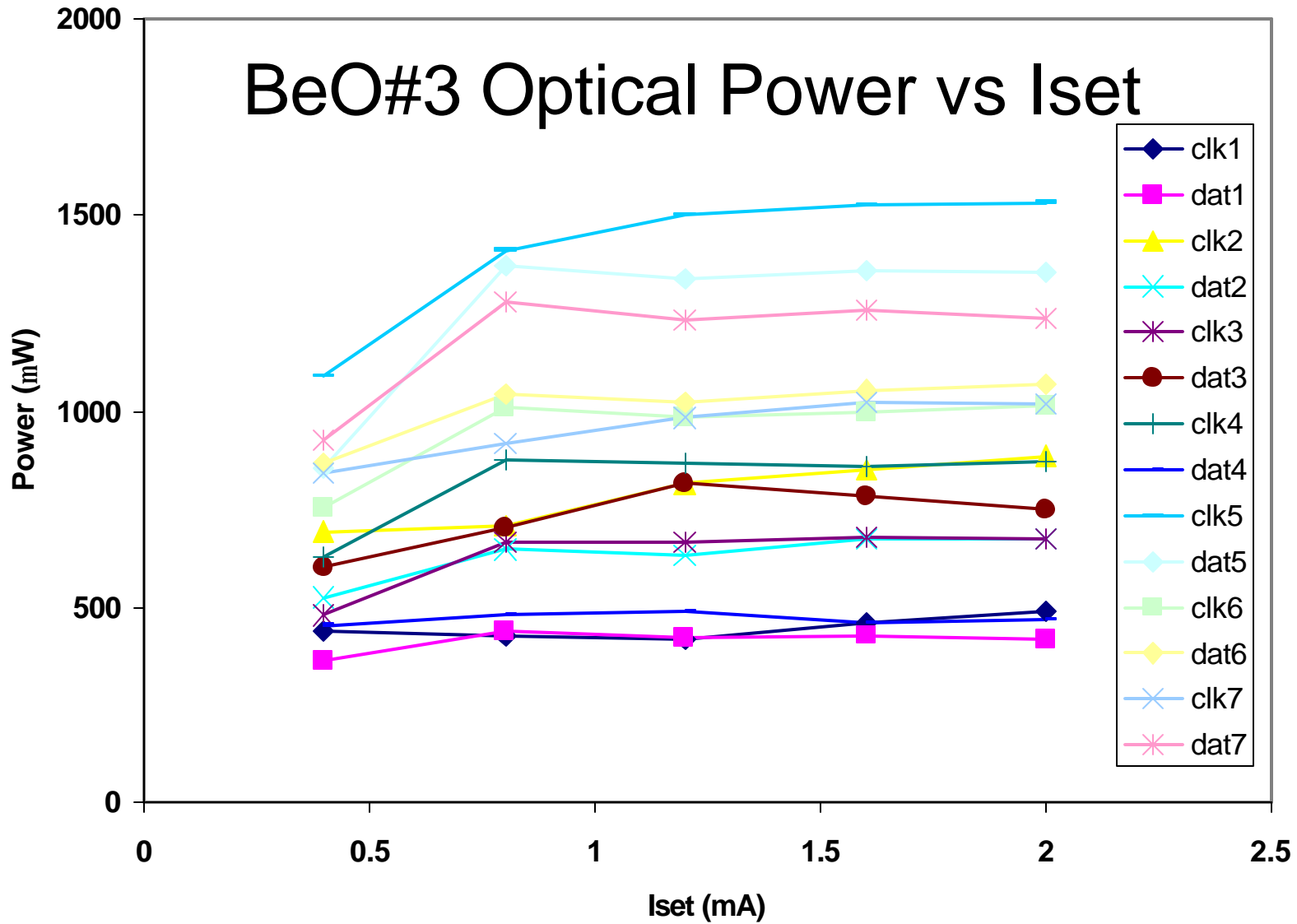


Opto-board #2 Clock Power vs Temp.



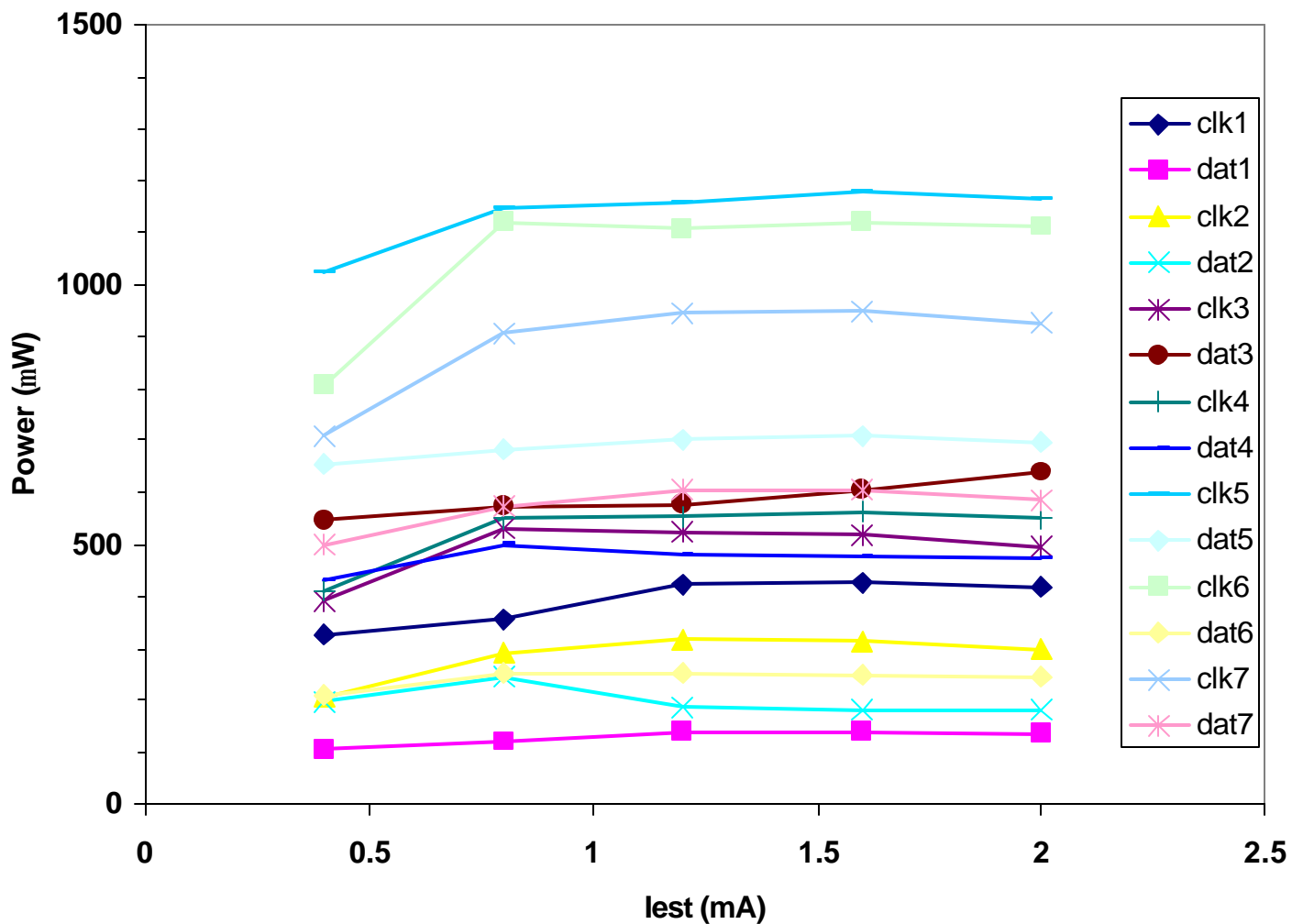
Opto-board #2 Data Power vs Temp.





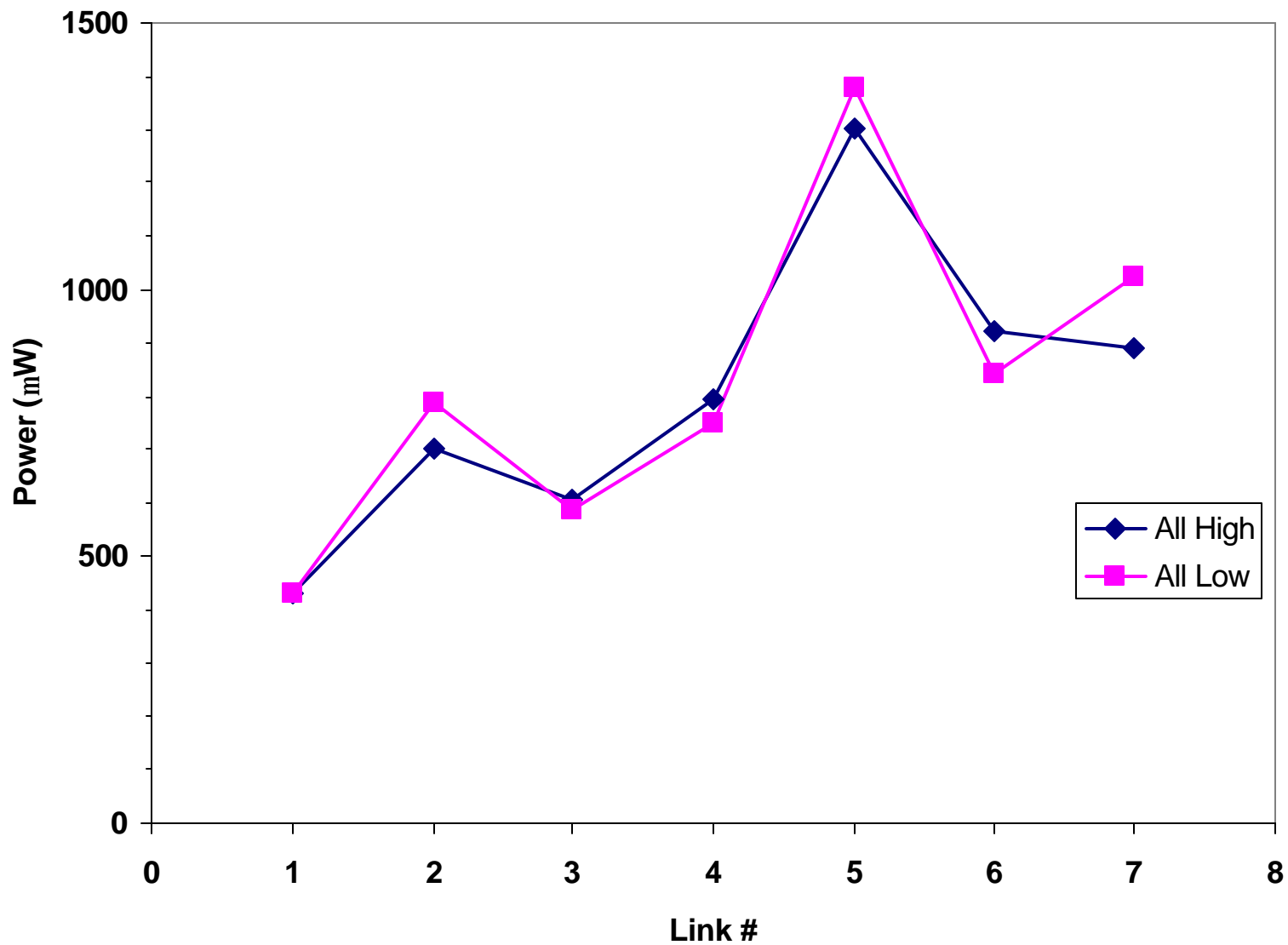
- measured at OSU with the long cable (wound).

BeO#4 Optical Power vs Iset

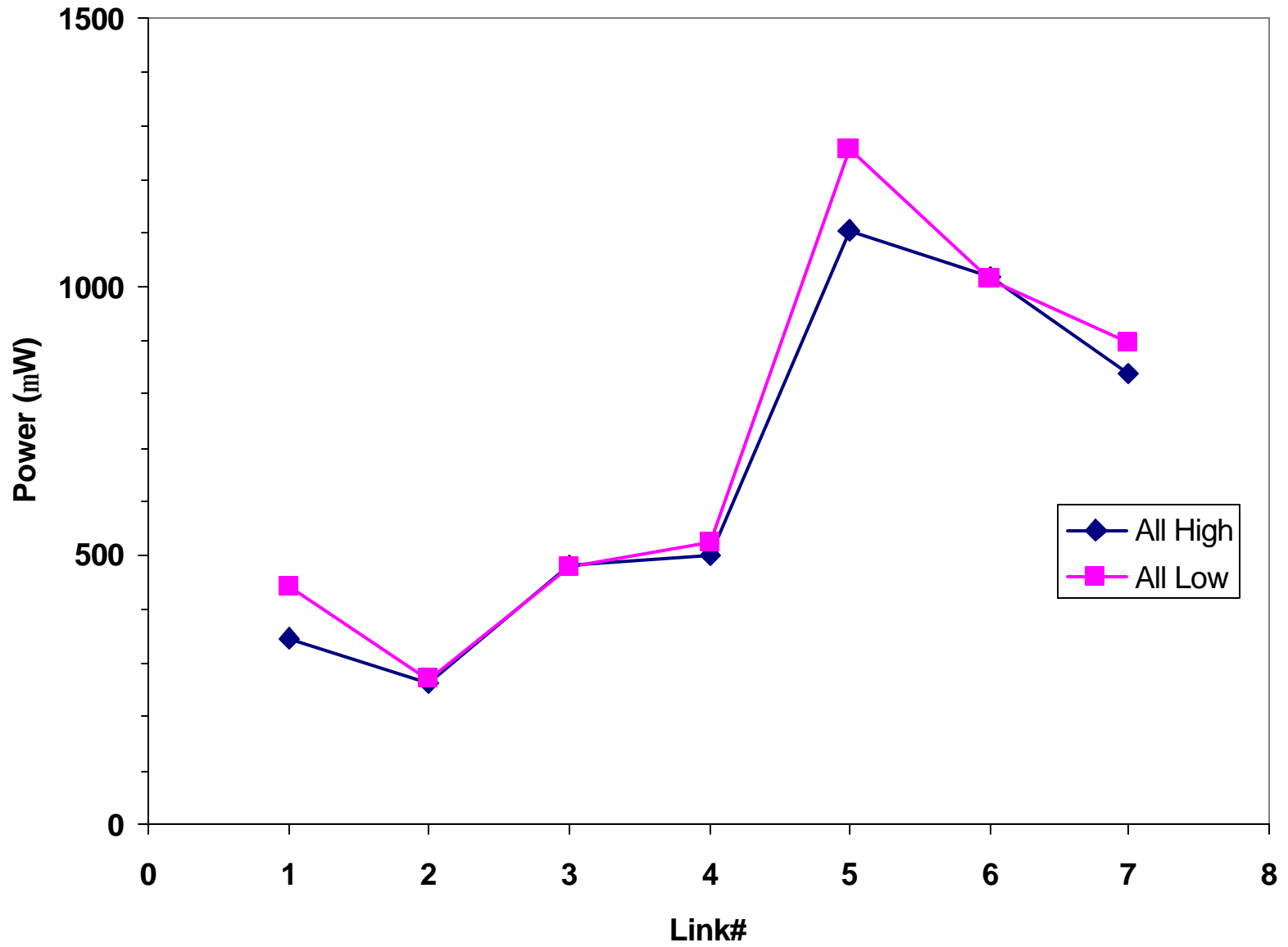


•measured at OSU with the long cable (wound).

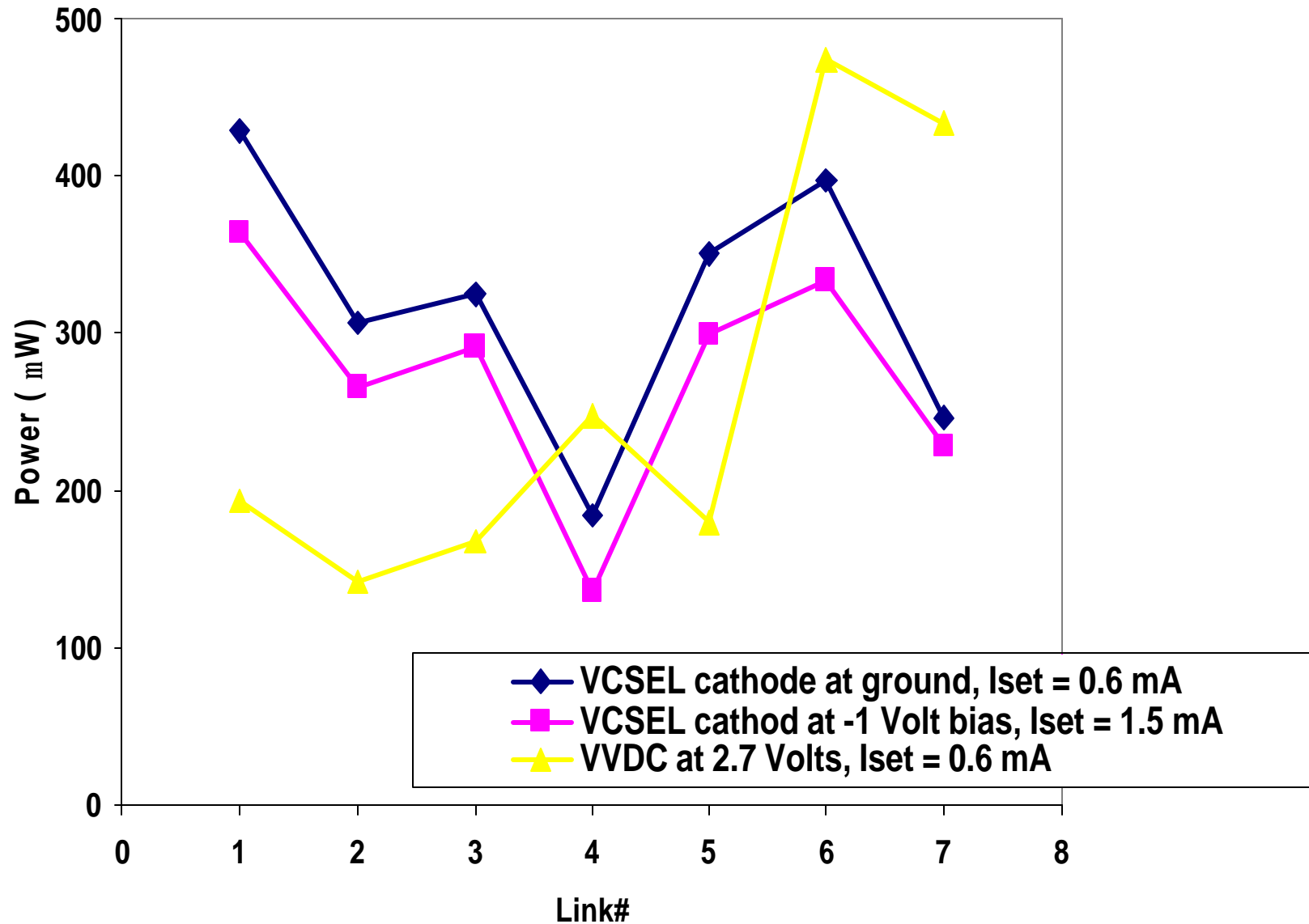
BeO#3 Clock Optical Power When Data All High and All low



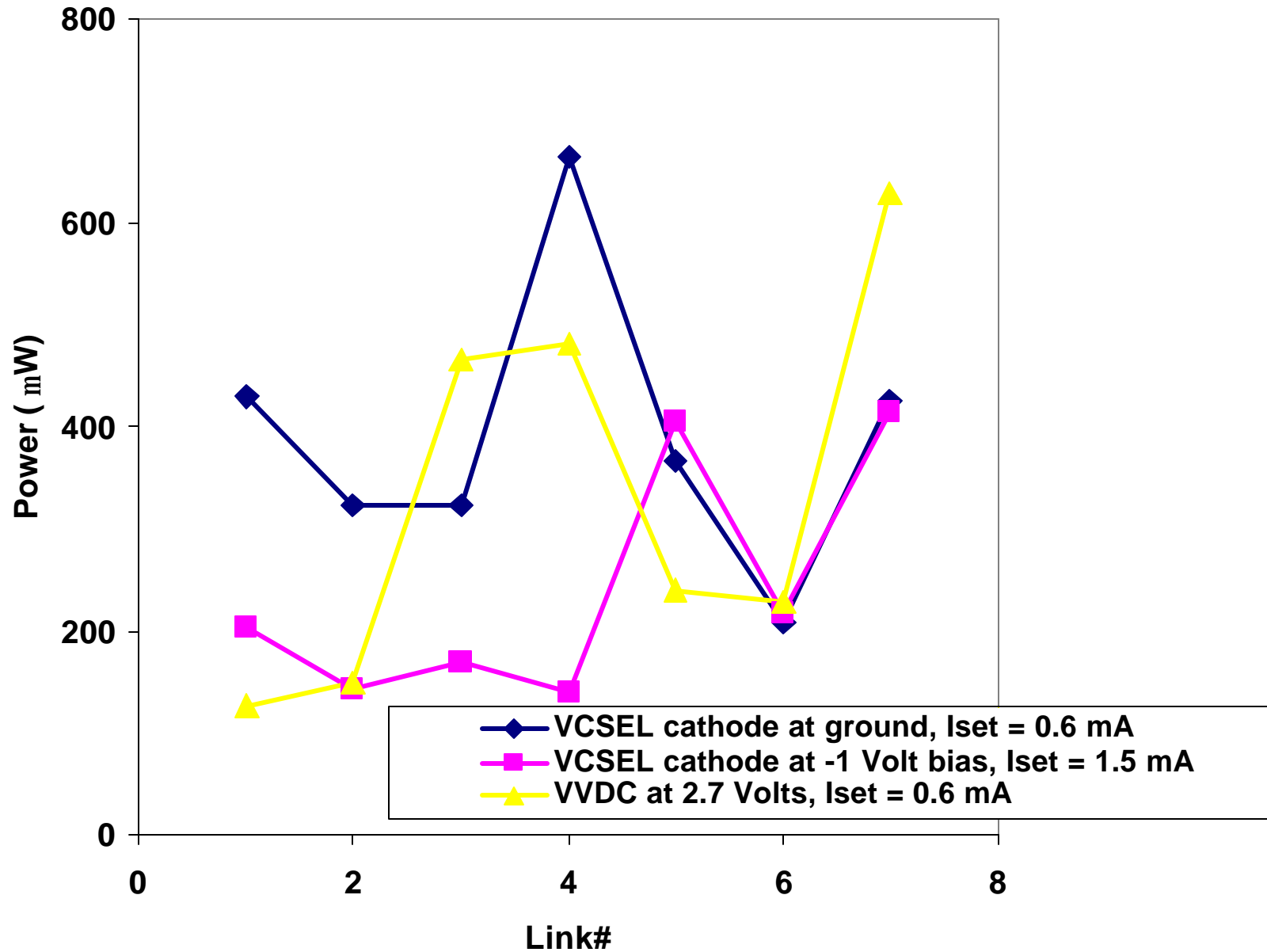
BeO#4 Clock Optical Power When Data All High and All low



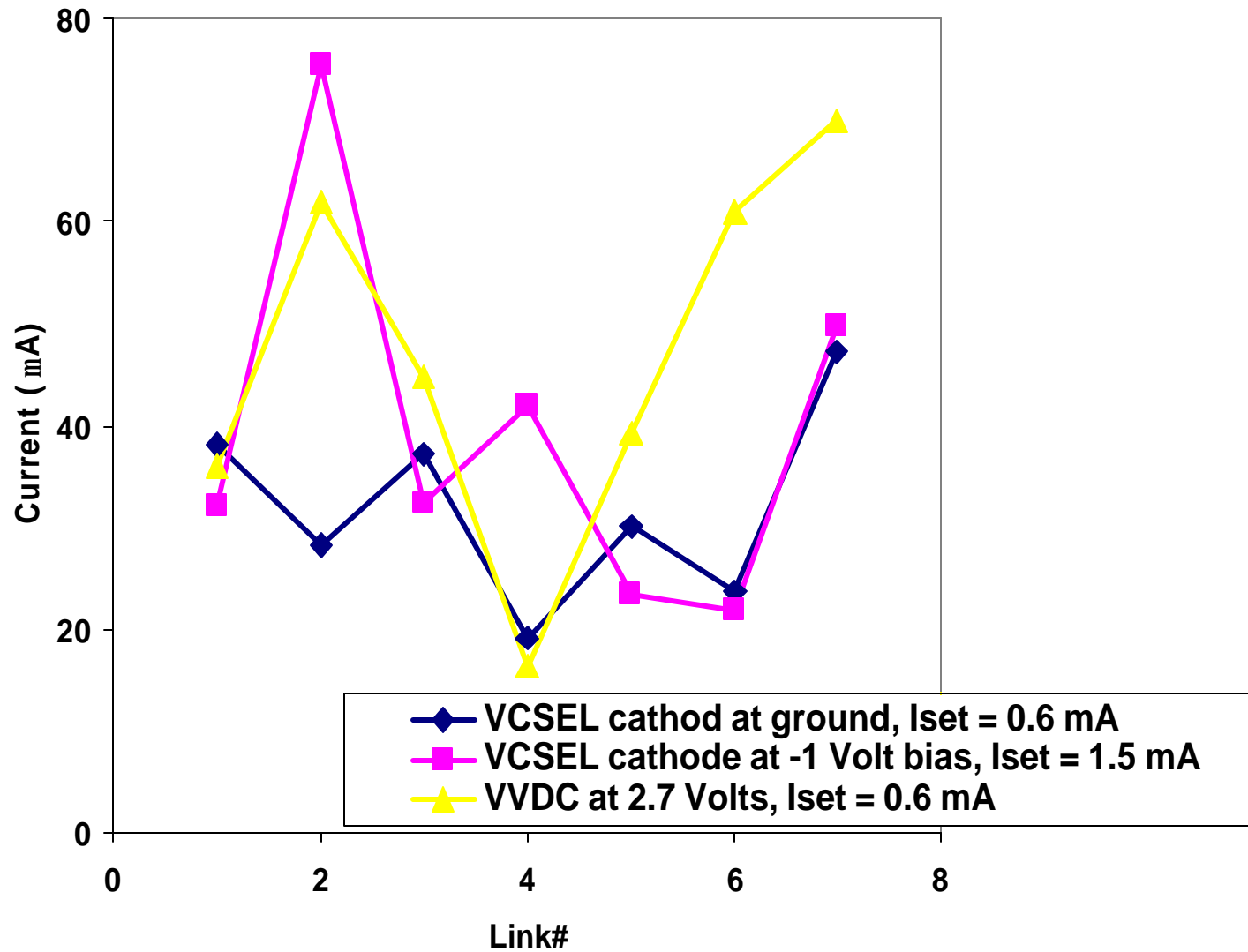
Clock Optical Power with Negative VCSEL Bias Voltage and VVDC = 2.7 V



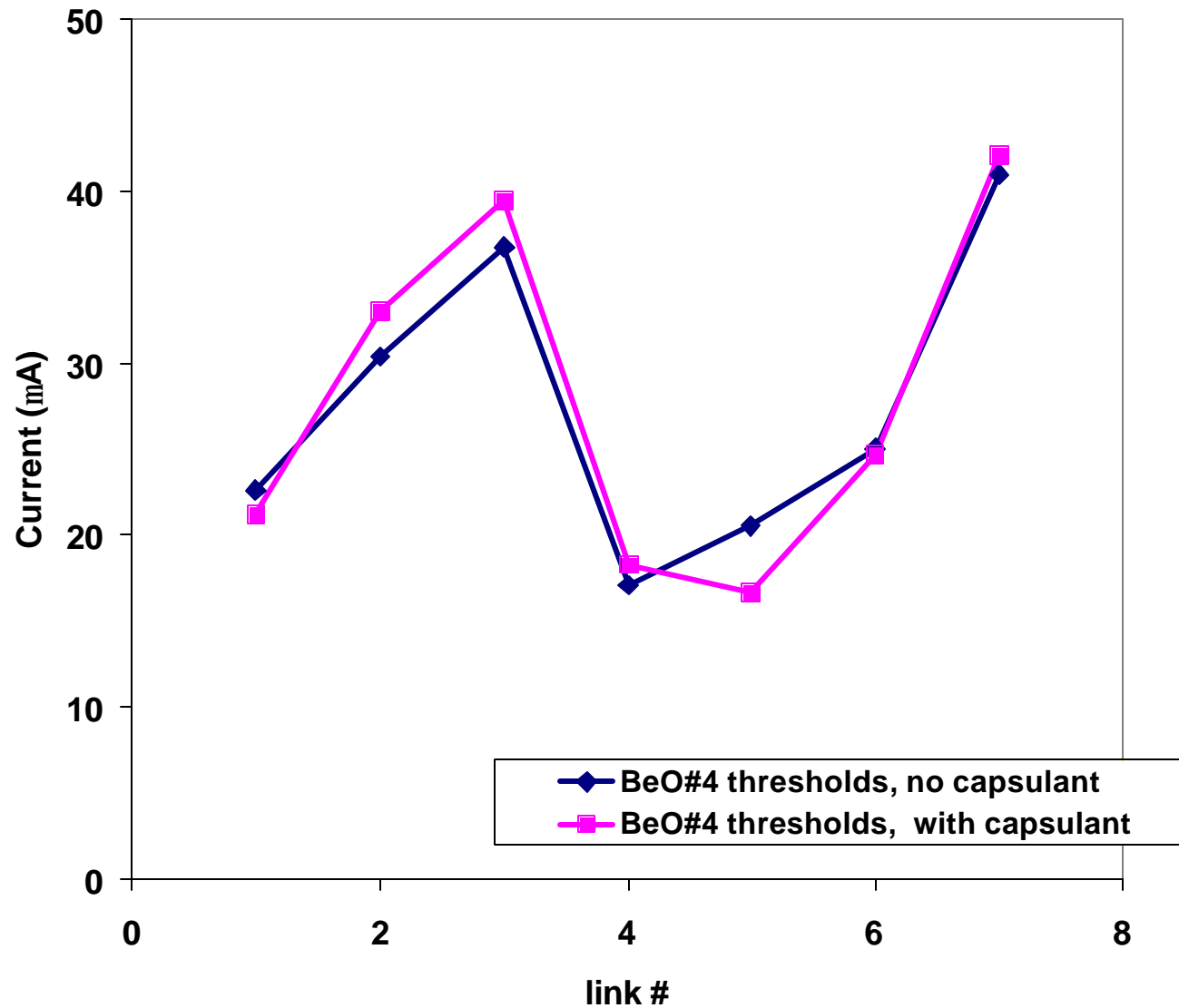
Data Optical Powers with Negative VCSEL Bias Voltage and $V_{VDC} = 2.7$ V



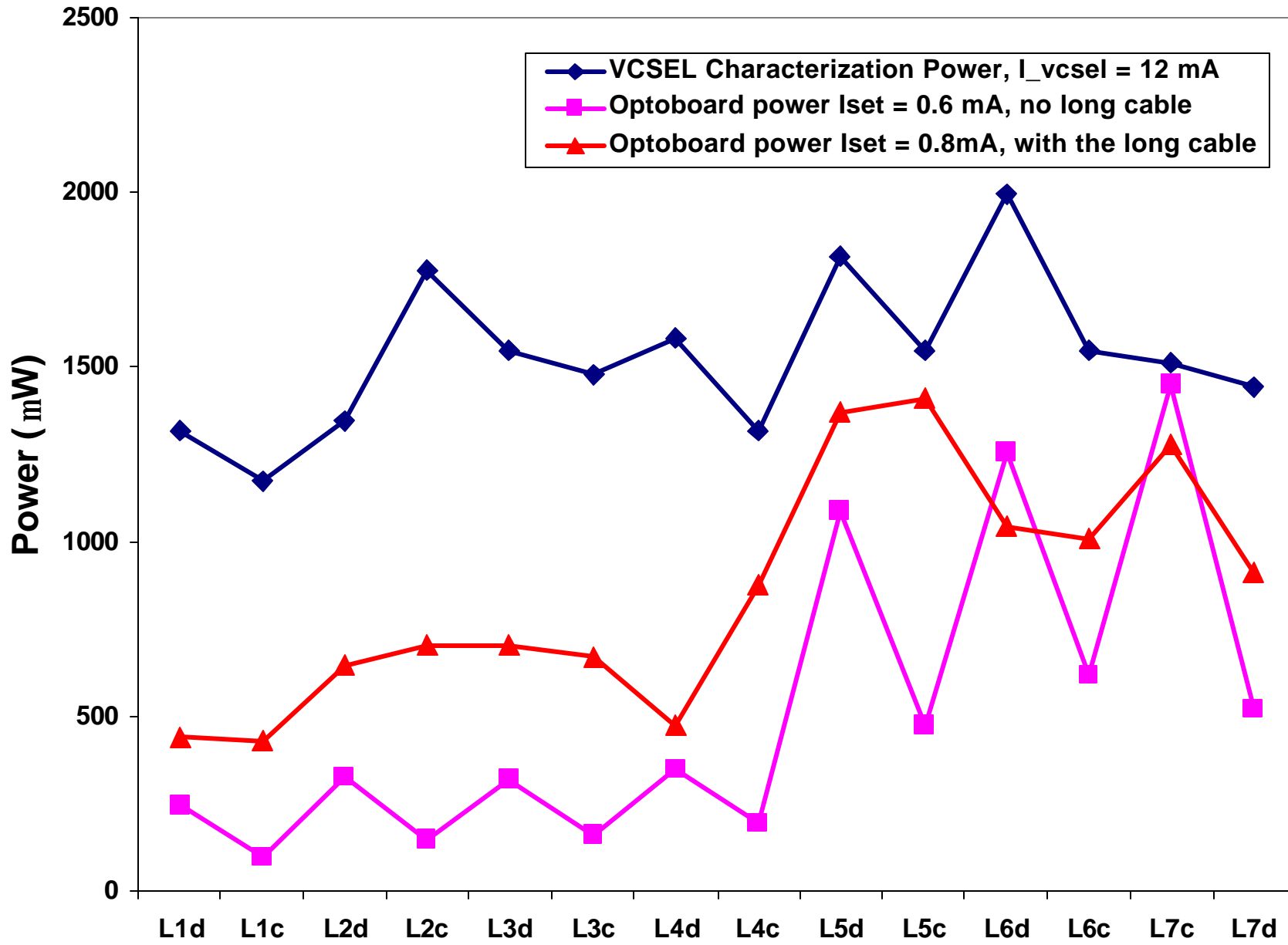
Thresholds with Negative VCSEL Bias Voltage and $V_{VDC} = 2.7\text{ V}$



BeO#4 Thresholds with and without encapsulant glue (ran for 1.5 hours at OSU)



BeO#3 Optical Power



BeO#4 Optical Power

