

Opto-board Prototype Status

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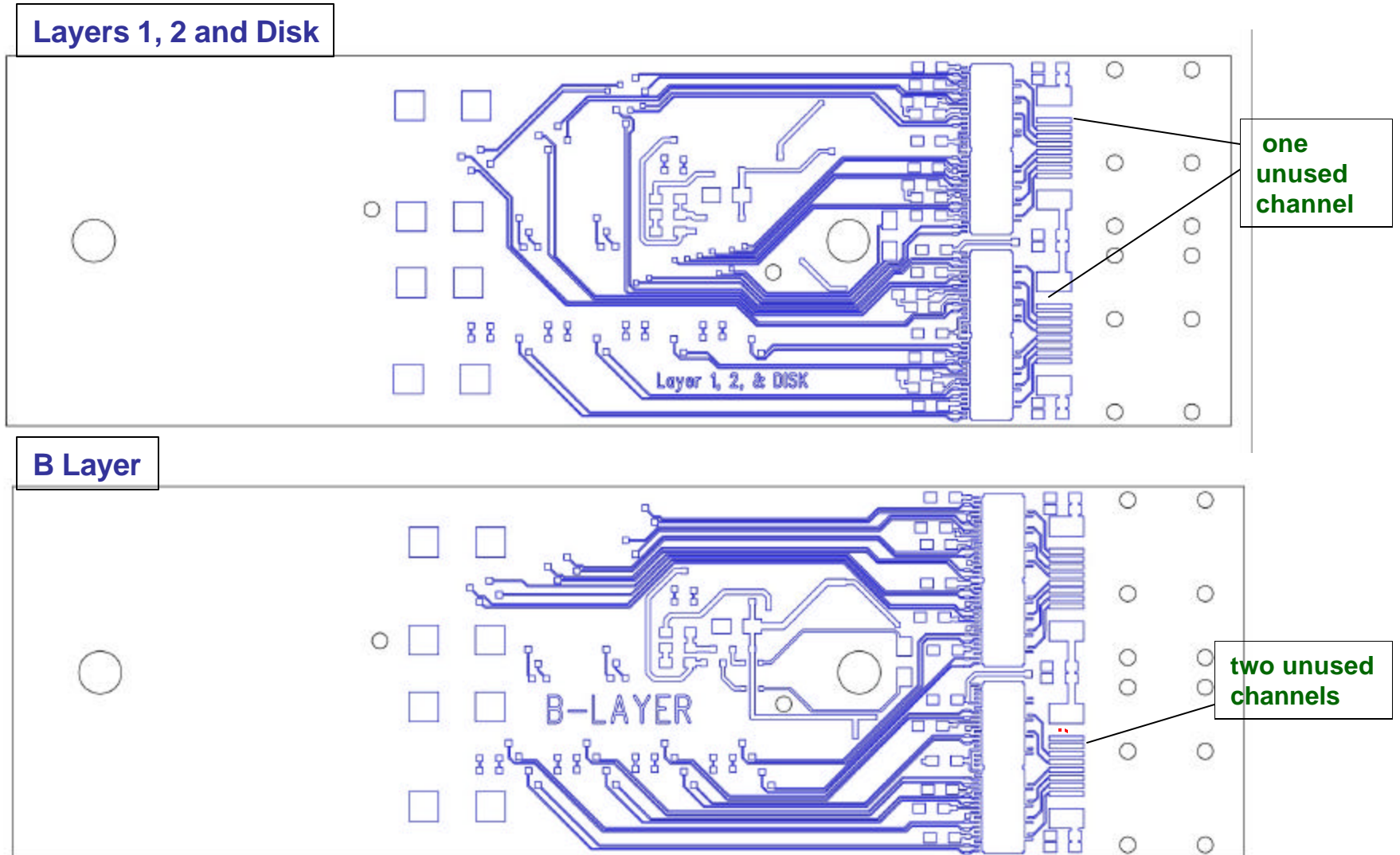
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December 15, 2003

Status of BeO Board Prototype

- Each opto-board is connected to 2 or 3 MT fiber ribbons
 - The attempt to assign the same channel as the unused one in each ribbon led us to realize that we need two opto-board flavors:
 - Layers 1, 2 and Disk:
 - Each module requires one link for transmitting data
 - ⇒ VCSEL opto-pack serves 7 links (7 modules)
 - Layer B:
 - Each module requires two links for transmitting data and both links should be in the same fiber ribbon
 - ⇒ VCSEL opto-pack#1 serves 8 links (4 modules)
 - ⇒ VCSEL opto-pack#2 serves 6 links (3 modules)
 - ⇒ Produce two opto-board flavors in this prototype run to save time and money

Opto-board Layout



Prototype Plan

- CPT shipment date: February 6, 2004
- Expect to receive ~12 BeO boards of each flavor
- Populate/test one BeO board of each flavor
 - If there is no design problem
 - ⇒ Send the remaining BeO boards to the vendor for loading of passive components
 - ⇒ Might have one board tested for each flavor by the February PRR

PDB Status

- Measurements as specified in the QA document will be stored in a table in PDB
 - Will be implemented by Polina
- Opto-board will have only one barcode sticker due to the limited space
 - A half-size sticker with four digits to identify the board
 - No barcode sticker for DORIC/VDC chips
 - No barcode sticker for PIN/VCSEL opto-packs