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make\_area

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SHELL=/bin/sh

```
# This file contains a set of rules used by the "make"
# command. This makefile $(MAKEFILE) tells "make" how the
# executable area $(COMMAND) should be created
# from the source files $(SRCS) via the object
# files $(OBJS) and the header files $(HDRS); type the command:
# "make -f make_program"
# where make_program should be replaced by the name of
# the makefile.
#
# This file also tells make what to do if arguments
# follow the "make" command.
#
# To remove the OBJS files; type the command:
# "make -f make_program clean"
#
# To create a gzipped tar file with name $(COMMAND).tarz
# containing this makefile and the SRCS files,
# type the command:
# "make -f make_program tarz"

SRCS= \
area.cpp

OBJS= \
area.o

HDRS= \

MAKEFILE= make_area

COMMAND= area

CC= g++
CFLAGS= -g -O3
WARNFLAGS= -Werror -Wall -W -Wshadow -fno-common
MOREFLAGS= -ansi -pedantic -Wpointer-arith -Wcast-qual -Wcast-align \
-Wwrite-strings -fshort-enums
LDFLAGS= -lgsl -lgslcblas

$(COMMAND): $(OBJS) $(MAKEFILE)
$(CC) -o $(COMMAND) $(OBJS) $(LDFLAGS) $(LIBS)

area.o : area.cpp $(MAKEFILE)
$(CC) $(CFLAGS) $(WARNFLAGS) -c area.cpp -o area.o

clean:
rm -f $(OBJS)

tarz:
tar zcf - $(MAKEFILE) $(SRCS) $(HDRS) > $(COMMAND).tarz
```