

June

Physics 517 Schedule

<i>Sun</i>	<i>Mon</i>	<i>Tue</i>	<i>Wed</i>	<i>Thu</i>	<i>Fri</i>	<i>Sat</i>
	1	2	3	4	5	6
7	8	9	10	11	12	13
14	15	16	17	18	19	20
21	22 Lecture 1: Circuit Analysis, Kirchoff's Laws, Thevenin and Norton Equivalentents	23 Intro. to Lab Lab 1: Instruments and Resistors	24 Lab1 (cont): Instrumentation and Resistors	25	26	27
28	29 Lecture 2: Capacitors (L)RC Circuits, AC Circuits Homework 1 Due	30 Lab1 (cont): Instrumentation and Resistors				

2009

July

Physics 517 Schedule

<i>Sun</i>	<i>Mon</i>	<i>Tue</i>	<i>Wed</i>	<i>Thu</i>	<i>Fri</i>	<i>Sat</i>
			1 Lab 1 (cont): Instrumentation and Resistors	2	3	4
5	6 Lecture 3: Capacitors (L)RC Circuits, AC Circuits <i>Lab 1 report due today, midnight</i>	7 Lab 2: LRC Circuits	8 Lab 2 (cont): LRC Circuits Homework 2 Due	9	10	11
12	13 Lecture 4: Inductors, Filters, Diodes, Power Supplies	14 Lab 3: Diodes and Rectifier Circuits	15 Lab 3 (cont): Diodes and Rectifier Circuits	16	17 <i>Lab 2 report due today, midnight</i>	18
19	20 Lecture 5: Transistor Circuits	21 Lab 4a: Transistor Characterics and Trans. Amplifiers Homework 3 Due	22 Lab 4a (cont): Transistor Characteristics, Trans. Amplifiers	23	24 <i>Lab 3 report due today, midnight</i>	25
26	27 Lecture 6: Operational Amplifiers	28 Lab 4a (cont): Transistor Characteristics, Trans. Amplifiers Homework 4 Due	29 Lab 4 b: AM Radio	30	31	

2009

August

Physics 517 Schedule

<i>Sun</i>	<i>Mon</i>	<i>Tue</i>	<i>Wed</i>	<i>Thu</i>	<i>Fri</i>	<i>Sat</i>
						1
2	3 Lecture 7: Digital Circuits, Boolean Algebra	4 Lab 4b (cont); AM Radio	5 Lab 4b (cont); AM Radio	6 Lab 5: Operational Amplifiers	7 <i>Lab 4 report due today, midnight</i>	8
9	10 Lecture 8: Digital Circuits, Memory, μ Processors	11 Lab 5 (cont.): Operational Amplifiers	12 Lab 5(cont.): Operational Amplifiers	13	14	15
16	17 Lab 5 (cont.): Operational Amplifiers	18 Lab 6a: Digital Circuits	19 Lab 6a (cont.): Digital Circuits	20	21 <i>Lab 5 report due today, midnight</i>	22
23	24 Lab 6b: Basic Stamp μ Processor	25 Lab 6b (cont.): Basic Stamp μ Processor	26 Lab 6b (cont.): Basic Stamp μ Processor	27 <i>Lab 6 report due today, midnight</i>	28	29

2009