

The BABAR LST Detector High Voltage System: Design and Implementation

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Abstract— In 2004, the first two sextants of the new Limited Streamer Tube (LST) detector were installed in the BaBar experiment to replace the ageing Resistive Plate Chambers (RPCs) as active detectors for the BABAR Instrumented Flux Return (IFR) muon system. Each streamer tube of the new detector consists of 8 cells. The cell walls are coated with graphite paint and a 100 μ m wire forms the anode. These wires are coupled in pairs inside the tubes resulting in 4 independent two-cell segments per LST. High voltage (HV) is applied to the 4 segments through a custom connector that also provides the decoupling capacitor to pick up the detector signals from the anode wires. The BABAR LST detector is operated at 5.5 kV. The high voltage system for the LST detector was designed and built at The Ohio State University (OSU HVPS). Each of the 25 supplies built for BaBar provides 80 output channels with individual current monitoring and over current protection. For each group of 20 channels the HV can be adjusted between 0 and 6 kV. A 4-fold fan-out is integrated in the power supplies to provide a total of 320 outputs. The power supplies are controlled through built-in CANbus and Ethernet (TCP/IP) interfaces.

In this presentation we will discuss the design and novel features of the OSU HVPS system and its integration into the BABAR EPICS detector control framework. Experience with the supplies operation during the LST extensive quality control program and their performance during the initial data taking period will be discussed.

I. INTRODUCTION

LIMITED Streamer Tubes were chosen to replace the rapidly ageing Resistive Plate Chambers (RPCs) in the Instrumented Flux Return (IFR) muon system [1] of the BABAR detector [2]. The LST principle of operation is based on the ionization caused by a charged particle crossing the tube. The ionization causes a streamer discharge in the gas volume

between the anode wire and the graphite coating of the cell and the induced signal on the HV wire can be measured. Since the High Voltage (HV) requirements of the new limited streamer tubes were incompatible with the previous detector, a new HV system had to be designed.

II. HIGH VOLTAGE REQUIREMENTS

The BABAR LSTs are operated with a gas mixture of Argon, Isobutane, and Carbon Dioxide (3:8:89). The active HV region ranges between 5 and 6kV, with a typical working point of 5.5kV. At this operating voltage, currents in a single LST tube typically range between 15 and 100nA without beams, and between 50 and 1000nA (depending on the position within the detector) with colliding beams. Since the occupancy and hence the current will vary as function of the distance to the beam pipe it was a requirement that the HV operating point can be adjusted at least on a layer by layer basis.

The BABAR LST detector consists of 1164 tubes, each of them split further into 4 two-cell segments, for a total of 4656 independent HV detection elements, requiring an HV system with a very large number of outputs. The high level of granularity makes the LST detector, as a whole, less sensitive to a single cell failure if the high granularity is matched by the HV system and individual HV channels can be disconnected or treated separately.

The current drawn by an individual LST tube is a critical parameter to monitor the performance of the detector. Therefore, independent current monitors for each of the 1164 detector elements are required. Each current has to be measured with a precision better than 10 nA at a rate of 10 Hz or more.

A sudden current increase to values higher than 2000nA is generally a sign of a self-sustaining discharge process in one of the tube cells. A hardware overcurrent protection, that stops the discharge by automatically lowering the high voltage, is necessary to protect the detector. For other high current situations such as high, beam-induced backgrounds, a sophisticated and configurable trip logic needs to be implemented.

While high granularity in terms of HV outputs and of current monitoring is necessary, independent HV operation of the individual two-cell segments within a tube is not needed. Furthermore, HV control granularity is reduced by the characteristics of the BABAR detector, where the tubes are radially distributed in 12 layers, containing up to 20 tubes

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each. Since the current drawn by a tube is proportional to the counting rates, i.e. the exposure to radiation and background, layers closer to the interaction point are expected to draw higher current than the outer ones. High currents are damaging in terms of the tubes lifetime, so the HV output must be adjustable, in order to allow minimization of the currents without compromising detection efficiency.

Finally, the LST HV system has to be fully integrated in the *BABAR* detector control system which is based on the EPICS framework.

III. OSU HIGH VOLTAGE POWER SUPPLY

The Ohio State University High Voltage power supply (OSU HVPS, Fig. 1) is a cost effective solution that fulfills all of the requirements listed in the previous section. It was designed to provide 320 HV outputs, with variable output voltage between 0 and 6kV. In terms of HV control the outputs are grouped into 4 independent HV groups of 80 outputs. Groups of 4 outputs are connected to one of 80 current monitor circuits in the power supply. This matches the design of our LST tubes where we have the 8 anode wires combined internally to 4 readout/HV channels. As a result we have one current monitor per LST tube. Even though the 4 outputs are driven in parallel by the power supply, having 4 independent output connections preserves the ability to disconnect and treat separately the individual 2-wire segments of the LST tubes.



Fig. 1. OSU HVPS with top cover and RF shields removed. One current monitor module has been extracted.

A. Current Measurement

At the hardware level the current monitoring is carried out by 80 current monitor modules (Fig. 2). Each module is capable of measuring a full scale current of $12\mu\text{A}$ with a resolution of 1.5nA .



Fig. 2. OSU HVPS current monitoring module.

Each module consists of a floating +5V (DC) supply, a low power ADC circuit, and output protection circuitry. The floating +5V (DC) supply is referenced to the module individual output voltage. This allows the current measurement circuitry to operate at all output voltage settings and protects the circuit during unexpected shorts on the output. The floating ADC circuit (Fig. 3) consists of a sense resistor in series with the output, an operational amplifier to measure and buffer the voltage drop across the sense resistor, and a voltage controlled oscillator (VCO) that generates a frequency proportional to the measured voltage from the op amp.

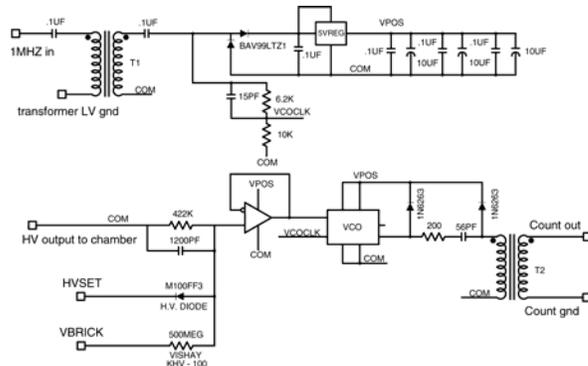


Fig. 3. OSU HVPS current monitoring module circuit diagram.

The signal from the VCO is referenced back to (low voltage) ground through an isolation transformer, where it is then amplified and its frequency counted by a Xilinx FPGA.

B. Overcurrent protection

An overcurrent protection circuit is part of each current monitoring module and is implemented using an HV diode as shown in Fig. 4.

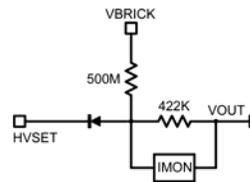


Fig. 4. Overcurrent protection circuit diagram

During normal operation, the output current is small and the HV diode is forward biased. The forward bias condition holds the output voltage equal to the set voltage plus the high voltage diode drop. When the output current rises above the overcurrent protection limit (I_{Thresh}) determined by the DC-DC converter voltage ($VBRICK$), the $500\text{M}\Omega$ resistor, and the high voltage set point ($HVSET$), the diode becomes reverse biased. The overcurrent protection limit is determined by:

$$I_{Thresh} = (VBRICK - HVSET) / 500\text{M}\Omega. \quad (1)$$

If the output current demand continues to increase, the output voltage drops linearly with a slope determined by the 500M Ω resistor.

This overcurrent protection mechanism is based on the LST operation principle to quickly and automatically recover tubes that went into self-sustaining discharge. The affected tube would start drawing a current well above I_{Thresh} , as a result its voltage will be lowered linearly, until it drops below the active HV region. When the discharge stops, the tube is automatically ramped back to its operational HV. This mechanism provides each channel with overcurrent protection without affecting any other channel in the same supply.

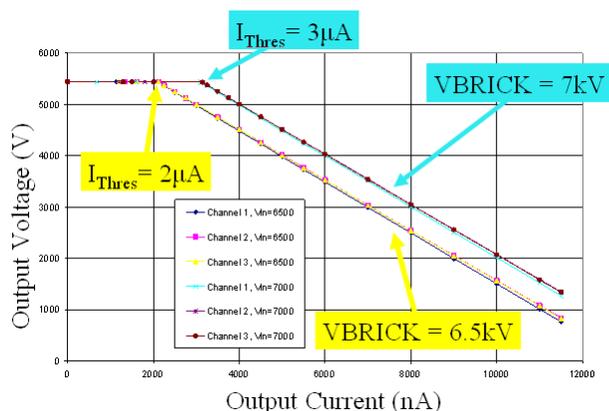


Fig. 5. Output voltage vs. output current for a HV channel of the OSU HVPS. The plot illustrates the overcurrent protection mechanism and its dependence from the DC-DC converter output voltage (VBRICK).

Since the internal high voltage DC-DC converter can be set remotely, it is possible to vary the current protection limit I_{Thresh} to adapt to in changing operating conditions, without modifying the hardware (Fig. 5).

C. Voltage Regulation

The HV rail for each HVPS is supplied by an internal Ultraviolet DC-DC converter that can provide up to 10kV at 3mA [3]. The individual HV group voltages are derived from the DC-DC converter and controlled by a Xilinx FPGA through 12-Bit DACs (Fig.6). The analog output voltage from each DAC is used as a reference for an operational amplifier. The operational amplifier uses a precision voltage divider network to obtain a measurement of the output voltage and sets the current in a high voltage BJT. The current in the BJT flows through a string of FET's, used to safely divide the output voltage, and through the high voltage input resistor from the DC-DC converter. When the current is set to a large value in the BJT and FET string, the voltage drop across the input resistor makes the high voltage output go low. When little current is flowing in the BJT and FET string, the drop on the input resistor is small and the individual segment voltage is close to that of the DC-DC converter. To provide a measure of the individual output voltages, an ADC circuit similar to

the one used on the current measurement modules is attached to the same precision voltage divider network mentioned above. The HVPS is capable of measuring the individual output voltages with 1V resolution.

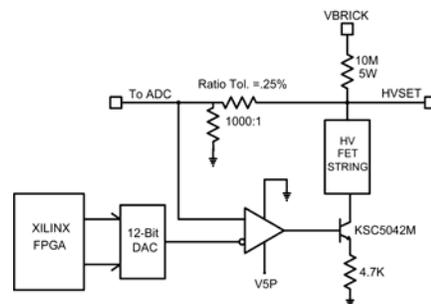


Fig. 6. Voltage regulation circuit diagram.

D. Back-Panel Fan-Out

It was already mentioned that the BaBar LST tubes consist of 8 individual cells and that pairs of cells are combined to form 4 readout/HV channels. In order to match this design we integrated a 1-to-4 fan-out into back-panel of the HV power supply system.

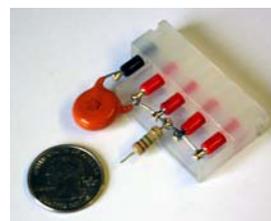


Fig.7 shows the custom designed connector with four 2-mm Banana plugs (HV) and one ground connections embedded in an injection-molded polyethylene piece.

E. Digital board and embedded software

A Xilinx FPGA performs the data collection and the control signal generation. A Rabbit microcontroller, RCM 3200 [4], is interfaced to the Xilinx chip to control the power supply and to implement higher level logic and communication functions. Calibration information and settings are kept in the microcontroller flash memory. The microcontroller processes the current and voltage information provided by the Xilinx to implement the ramping and trip logic. The command structure is implemented at the microcontroller level using the two communication interfaces available, Ethernet (TCP/IP) and CANbus. The software was fully developed at The Ohio State University, and the ability to modify and continue development to adapt to experimental necessities is a definite advantage over a commercial solution.

F. Trip and Ramping logic

In order to protect the LST system from high current situations that could damage the detector, the OSU HVPS include a sophisticated, configurable trip logic covering three possible trip mechanisms: current over threshold for a user

selectable amount of time, instantaneous current over threshold (spike), and an over-current situation for the internal Ultravolt HV DC-DC converter. Both the time over threshold and the spike trip are implemented for each individual output channel. For the time over threshold trip logic, trip levels and time over threshold limits can be differentiated for ramping and non-ramping conditions.

IV. DETECTOR CONTROLS

The OSU HVPS controls are fully integrated in the *BABAR* EPICS detector control system using the CANbus interface. A dedicated PowerPC MVME5500 Input Output Controller (IOC) running RTEMS [5] is used to run the LST HV EPICS application. The EPICS controls allow prompt reaction to changing beam conditions and safe operation of the LST detector during injection cycles. The detector controls include an integrated alarm system with configurable warning and alarm levels for all quantities monitored by the OSU HVPS. An archiving function is used to record the monitored quantities in a database. A Graphical User Interface is available to operate the OSU HVPS in stand alone mode.

A series of opto-coupled input/output signals are provided on the front panel of the OSU HVPS to integrate the LST HV system in the BaBar hardware interlock system that prevents the accelerator from injecting beam when the detector conditions are not safe.

A redundant interlock system is implemented that inhibits turning on high voltage when conditions are not safe: an external HV enable signal controlled by BaBar, a front panel HV enable switch, a software HV enable, a switch lock power switch with a removable key. All these enables have to be present for the supply to provide HV output.

V. RUNNING EXPERIENCE

A total of 25 OSU HV power supplies have been built: 23 are located at the experiment at the Stanford Linear Accelerator Center (SLAC), 2 are kept at OSU. The use of the OSU HVPS allowed for a very successful LST Quality Control (QC) program. HV was a key component of the QC program and it was used to HV condition tubes and for burn-in tests, to take single rate plateaus curves and to run a scan test with a radioactive source.

Data taking with the new LST detector started in April 2005 and the new power supplies have performed successfully. So far, only the top and bottom sextants of the LST detector have been installed (the remaining sextants will be installed in 2006). The LSTs in each sextant are powered by 3 OSU HVPS. An extra OSU HVPS is used to study and treat any misbehaving tube from the two sextants.

Overall, the LST detector has performed very well. Only a few tubes showed signs of internal discharges and additional HV conditioning using the hospital supply recovered most of them. Very few channels (<0.5%) had to be permanently operated at a lower voltage.

The innermost layers drew very high currents as a function of luminosity (Fig. 8), so the first two layers tubes were split into two OSU HVPS channels. Extrapolating from Fig. 8, the current drawn per tube at a luminosity of $2 \times 10^{34} \text{ cm}^{-2} \text{ s}^{-1}$, which

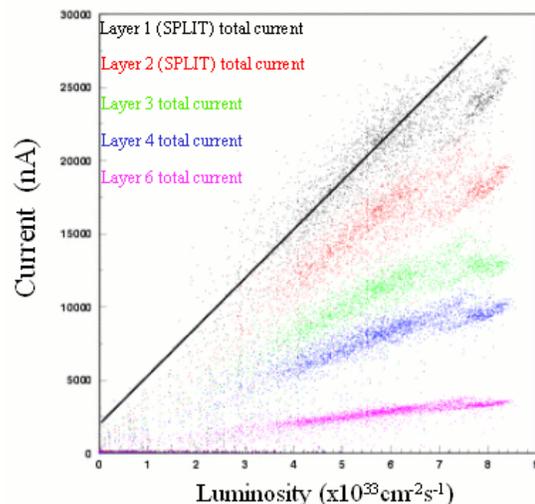


Fig. 8. Total layer current vs. luminosity. The plot shows how the current drawn by tubes is linearly dependent on luminosity. The slope for the innermost layers is much steeper than the middle layers. Extrapolating the current drawn per tube at $2 \times 10^{34} \text{ cm}^{-2} \text{ s}^{-1}$ would be approximately 8000nA per tube (a factor of 4 the original estimate).

would be the highest luminosity *BABAR* is expected to run at, is approximately 8000nA. This is a factor of 4 larger than originally expected, but with a firmware upgrade the OSU HV supplies will be able to power the LSTs even in this scenario.

VI. CONCLUSIONS

A new, multi-channel power supply system was developed for the BaBar LST detector. The OSU HVPS system has proven to be with a versatile and robust HV solution at much lower costs than other commercially available systems.

Performance during the QC and data taking is excellent, especially with respect to the flexibility of a custom solution, that allows the system to adapt changing conditions such as the higher than anticipated current running conditions.

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