

May. 17, 2004

Opto-board Assembly Status

Opto-board S/N:

Site (check one): OSU

Seigen

Opto-board flavor (check one): B-layer

Left

Right

VDC S/N				
DORIC S/N			--	--

Step	Date	Initial	Comments
1) 80-pin connector reinforcement			
2) Mechanical tolerance test			
3) Short Test			
4) Opto-board cleaning			
5) VDC mounting			
6) DORIC mounting			
7) VDC/wire-bond traces masking			
8) DORIC/wire-bond traces masking			
9) Measure VCSEL power			
10) Measure PIN responsivity			
11) Glue VCSEL opto-pack			
12) Glue housing			
13.1) Glue 2 nd VCSEL opto-pack (?)			
13.2) Glue housing (?)			
14.1) Glue PIN opto-pack			
14.2) Glue housing			
15) Solder VCSEL opto-pack			
16) Solder 2 nd VCSEL opto-pack (?)			
17) Solder PIN opto-pack			
18) Wire bonding			

19)	Basic functionality test			
20)	Encapsulation			
21)	Burn-in			
22)	Thermal cycle			
23)	QA			

