The 'B' signals are for in between pairs.

The ENOUT voltage divider circuit for LVDS input is common to all SCA's.

Stuff EITHER R26 or R27, NOT BOTH!
The 'B' signals are for in between pairs.

Generally, one filter CAP per power pin, one TANT per chip.

Watch placement of ADCCLK termination!

"V5PDADC"

"V5PAADC"

SCA- ONE 0.1UF AND 0.001UF PER POWER PIN
CFEB to DAQ MB AMP Connector

The PROMs are reprogrammed through JTAG instruction. The LCD indicates status of configuration.
One filter CAP per power pin, one TANT per chip.

For FPGA:

One filter CAP per power pin, one TANT per chip.
SCA ONE 0.1UF AND 0.001UF PER POWER PIN

- **R26** or **R27**, **NOT BOTH!**

**Group G2 (and GA1)**
The 'B' signals are for in between pairs.
Put the op-amps near the branch point of the traces!

Group G2

THE OHIO STATE UNIVERSITY
PHYSICS DEPARTMENT ELECTRONICS LAB
174 WEST 18TH AVE, COLUMBUS OH 43210

Cathode Comparator System
Cathode Front End Card
CMS CSC Electronics

JRG  PRINTING PAGE  REJECT
10-2-2001 9:43  CFEBVTX: 13
The 'B' signals are for in between pairs.

Stuff EITHER R26 or R27, NOT BOTH!
Consider SMT/Resettable fuses in the future...

The 'B' signals are for in between pairs.

Watch placement of ADCCLK termination!

Generally, one filter CAP per power pin, one TANT per chip.
Put the op-amps near the branch point of the traces!
Free CAP nos:
Norm: 185-
Spec G: 110-
Free RES nos:
Norm: 87-91, 97-
Spec G: 41-
Free UNIT nos:
Norm: 47-
Spec G: 19-
Free JUMPER nos:
Norm: 31-
Spec G: 8-

CALABRATION CAP ADDED TO EACH CHANNEL

One filter CAP per power pin, one TANT per chip.
The 'B' signals are for in between pairs.

SCA ONE 0.1UF AND 0.001UF PER POWER PIN

**CF AMP**

**Cathode Front End Card JRGUC**

**CMS CSC Electronics**

**Group G4 (and GA2)**

**Stuff EITHER R26 or R27, NOT BOTH!**
Consider SMT/Resettable fuses in the future...

The 'B' signals are for in between pairs.
Put the op-amps near the branch point of the traces!
The 'B' signals are for in between pairs.

Stuff EITHER R26 or R27, NOT BOTH!
The 'B' signals are for in between pairs.

Watching placement of ADCCLK termination!

Generally, one filter CAP per power pin, one TANT per chip.
Put the op-amps near the branch point of the traces!
The 'B' signals are for in between pairs.

Stuff EITHER R26 or R27, NOT BOTH!

The OHIO STATE UNIVERSITY
PHYSICS DEPARTMENT ELECTRONICS LAB
174 WEST 18TH AVE, COLUMBUS OH 43210

THE OHIO STATE UNIVERSITY
PHYSICS DEPARTMENT ELECTRONICS LAB
174 WEST 18TH AVE, COLUMBUS OH 43210

UCDavis SCA3B
Cathode Front End Card
CMS CSC Electronics

10-2-2001 9:44
CFEBVTX
CFEBVTX. 27
Generally, one filter CAP per power pin, one TANT per chip.
Put the op-amps near the branch point of the traces!