Opto-board Assembly Status

Opto-board S/N:  
Opto-board flavor (check one): O B-layer  
VDC S/N  
DORIC S/N  
Site (check one): O OSU  
O Left  
O Right  
O Seigen

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80-pin connector reinforcement  
Mechanical tolerance test  
Short Test  
Opto-board cleaning  
VDC mounting  
DORIC mounting  
VDC/wire-bond traces masking  
DORIC/wire-bond traces masking  
Measure VCSEL power  
Measure PIN responsivity  
Glue VCSEL opto-pack  
Glue housing  
Glue 2\textsuperscript{nd} VCSEL opto-pack (?)  
Glue housing (?)  
Glue PIN opto-pack  
Glue housing  
Solder VCSEL opto-pack  
Solder 2\textsuperscript{nd} VCSEL opto-pack (?)  
Solder PIN opto-pack  
Wire bonding
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**Opto-board QA data Sheet: Optical Power and Responsivity**

**Opto-board S/N:**

**VCSEL Opto-pack S/N:**

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**VCSEL Opto-pack S/N:**

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Opto-board QA Data Sheet

Opto-board S/N:

Current consumption:

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<th>Max</th>
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<td>217</td>
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* The allowable range for current consumption for a B-layer opto-board is 330-400 mA.

Dark current:

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Optical:

<p>|       | Unit | Max | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | Pass/fail? |
|-------|------|-----|---|---|---|---|---|---|---|---|---|----|----|----|----|------------|
| Rise  | ns   | 1   |   |   |   |   |   |   |   |   |   |    |    |    |    |            |
| Fall  | ns   | 1   |   |   |   |   |   |   |   |   |   |    |    |    |    |            |</p>
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<td>Clock- common mode</td>
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